ESE 3400: Medical Devices Lab

Lec 13: October 31, 2022 Data Converters Pt 3: ADC Oversampling and Noise Shaping



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- Oversampling
- Noise Shaping
- Sigma-Delta ADCs

ADC Architectures



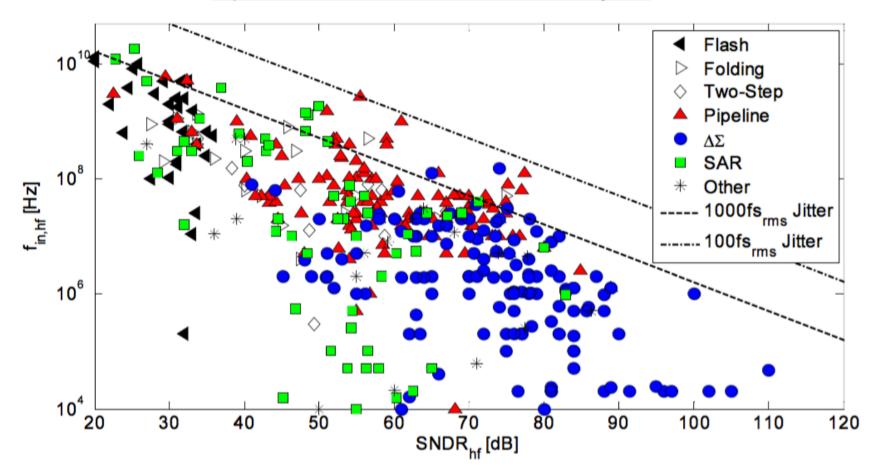


- □ Word-at-a-time
 - E.g. flash ADC
 - Instantaneous comparison with 2^B-1 reference levels
- Multi-step
 - E.g. pipeline ADCs
 - Coarse conversion, followed by fine conversion of residuals
- Bit-at-a-time
 - E.g. successive approximation ADCs
 - Conversion via a binary search algorithm

speed



Data: http://www.stanford.edu/~murmann/adcsurvey.html





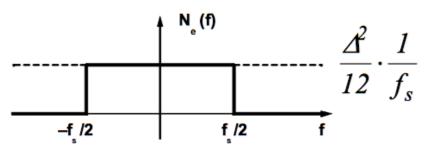
• Assuming full-scale sinusoidal input, we have

 $SNR_Q = 6.02B + 1.76 dB$

B (Number of Bits)	SQNR
8	50dB
12	74dB
16	98dB
20	122dB

Quantization Noise Spectrum

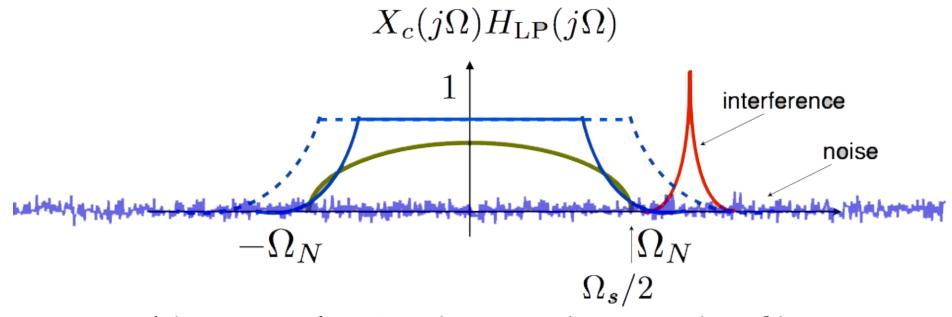
 If the quantization error is "sufficiently random", it also follows that the noise power is uniformly distributed in frequency



References

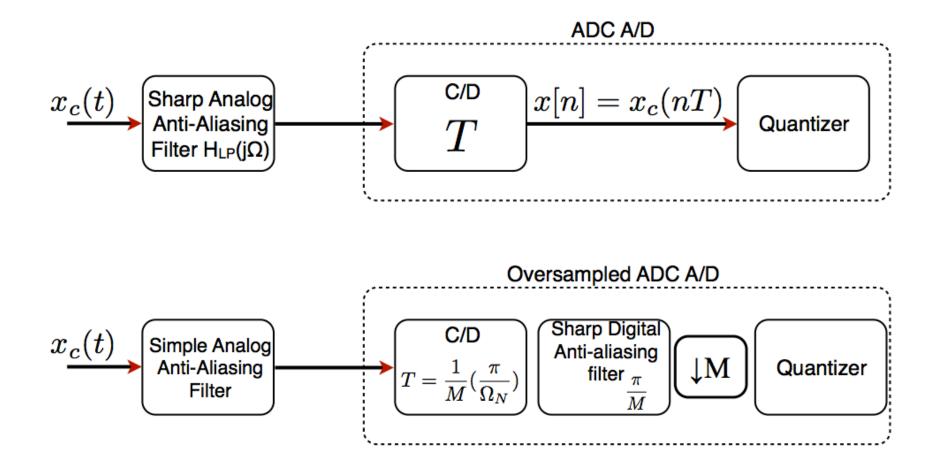
- W. R. Bennett, "Spectra of quantized signals," Bell Syst. Tech. J., pp. 446-72, July 1988.
- B. Widrow, "A study of rough amplitude quantization by means of Nyquist sampling theory," IRE Trans. Circuit Theory, vol. CT-3, pp. 266-76, 1956.



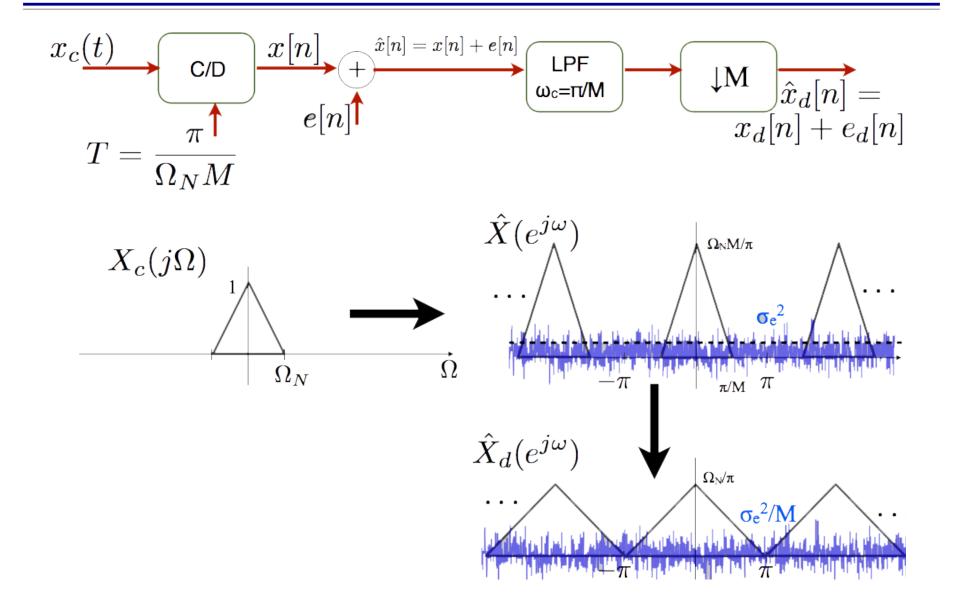


- □ Problem: Hard to implement sharp analog filter
- Consequence: Crop part of the signal and suffer from noise and interference



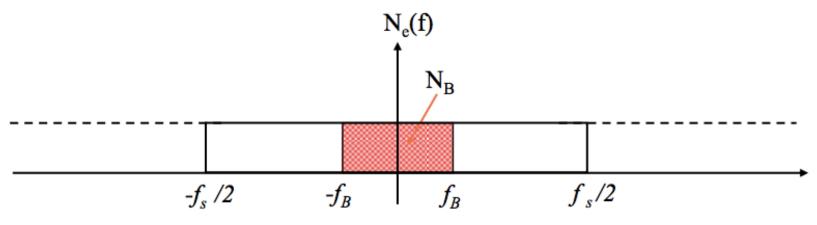


Quantization Noise with Oversampling



ADC Converters Baseband Noise

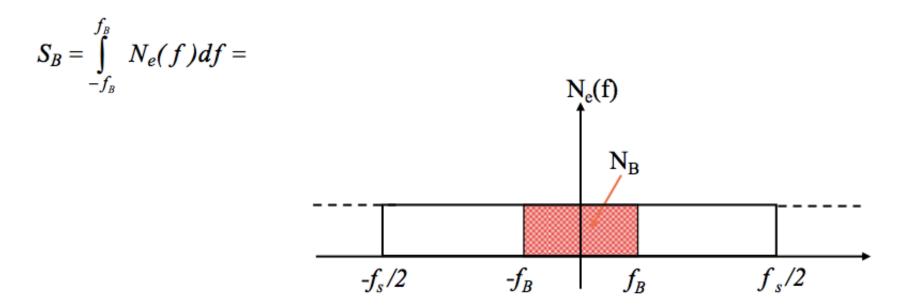
- $\hfill\Box$ For a quantizer with quantization step size Δ and sampling rate f_S :
 - Quantization noise power is distributed uniformly across Nyquist bandwidth $(f_S/2)$



• Power spectral density: $N_e(f) = \frac{\overline{e^2}}{f_s} = \left(\frac{\Delta^2}{12}\right) \frac{1}{f_s}$

• Noise is distributed over the Nyquist band $-f_S/2$ to $f_S/2$

• Oversampled Converters Baseband Noise



Oversampled Converters Baseband Noise

$$S_{B} = \int_{-f_{B}}^{f_{B}} N_{e}(f)df = \int_{-f_{B}}^{f_{B}} \left(\frac{\Delta^{2}}{12}\right)\frac{1}{f_{s}}df$$

$$= \frac{\Delta^{2}}{12}\left(\frac{2f_{B}}{f_{s}}\right)$$
where for $f_{B} = f_{s}/2$

$$S_{B0} = \frac{\Delta^{2}}{12}$$

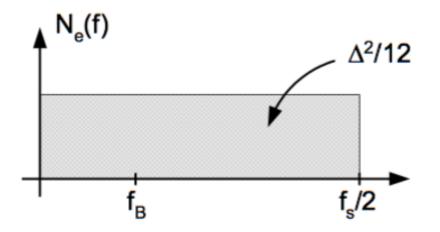
$$S_{B0} = S_{B0}\left(\frac{2f_{B}}{f_{s}}\right) = \frac{S_{B0}}{M}$$
where $M = \frac{f_{s}}{2f_{B}} = oversampling ratio$

Oversampled Converters Baseband Noise

$$S_{B} = S_{B0} \left(\frac{2f_{B}}{f_{s}} \right) = \frac{S_{B0}}{M}$$

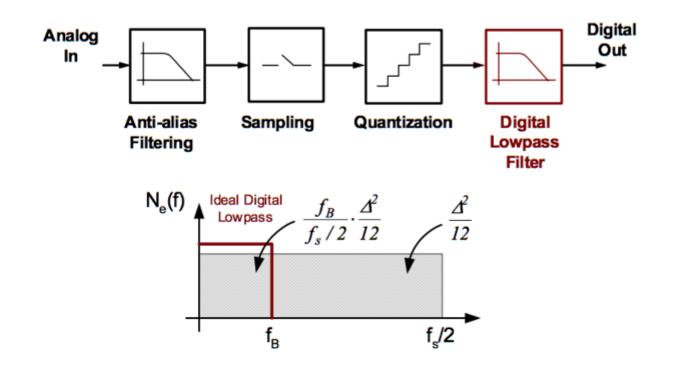
where $M = \frac{f_{s}}{2f_{B}} = oversampling \ ratio$
2X increase in M
 \rightarrow 3dB reduction in S_B
 $\rightarrow \frac{1}{2}$ bit increase in resolution/octave oversampling





- Recall that the "noise" introduced by quantizer is evenly distributed across all frequencies
 - Provided that quantization error sequence is "sufficiently random"
- □ Idea: Let's filter out the noise beyond $f=f_B$





□ Total quantization noise at digital output is reduced proportional to "oversampling ratio" $M=(f_S/2)/f_B$



- □ Increasing M by 2x, means a 1/2 bit increase in resolution
 - "1/2 bit per octave" (octave means doubling in frequency)
- □ Is this useful?
- **•** Reality check
 - Want 16-bit ADC, f_B=1MHz
 - Use oversampled 8-bit ADC with digital lowpass filter



- □ Increasing M by 2x, means a 1/2 bit increase in resolution
 - "1/2 bit per octave" (octave means doubling in frequency)
- □ Is this useful?
- Reality check
 - Want 16-bit ADC, f_B=1MHz
 - Use oversampled 8-bit ADC with digital lowpass filter
 - 8-bit increase in resolution necessitates oversampling by 16 octaves

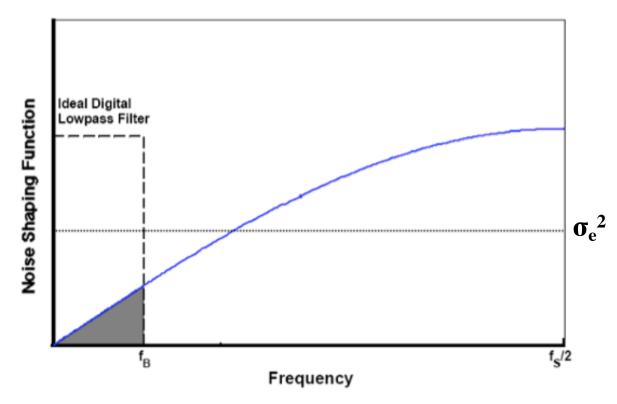
$$f_s \ge 2 \cdot f_B \cdot M = 2 \cdot 1MHz \cdot 2^{16}$$
$$\ge 131GHz$$

Noise Shaping



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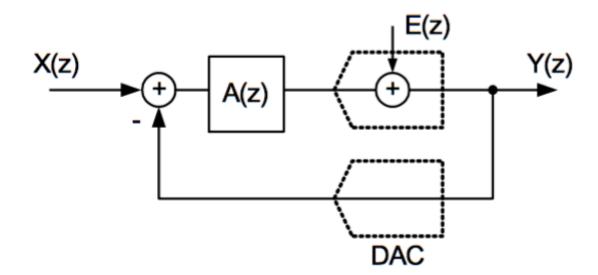




 Idea: "Somehow" build an ADC that has most of its quantization noise at high frequencies

• Key: Feedback







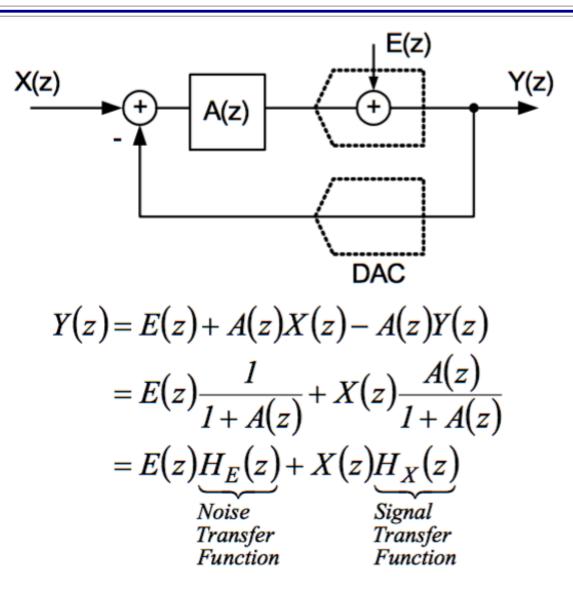
Define the **forward z-transform** of x[n] as

$$X(z) ~=~ \sum_{n=-\infty}^{\infty} x[n] \, z^{-n}$$

- □ The core "basis functions" of the z-transform are the complex exponentials zⁿ with arbitrary z ∈ C; these are the eigenfunctions of LTI systems for infinite-length signals
- Notation abuse alert: We use X(•) to represent both the DTFT X(e^{jω}) and the z-transform X(z); they are, in fact, intimately related

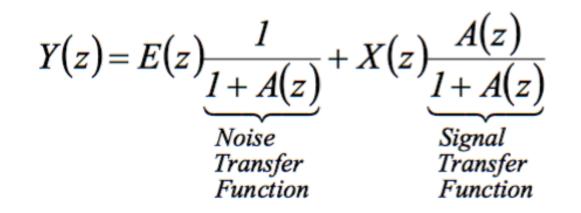
$$X_z(z)|_{z=e^{j\omega}} = X_z(e^{j\omega})$$





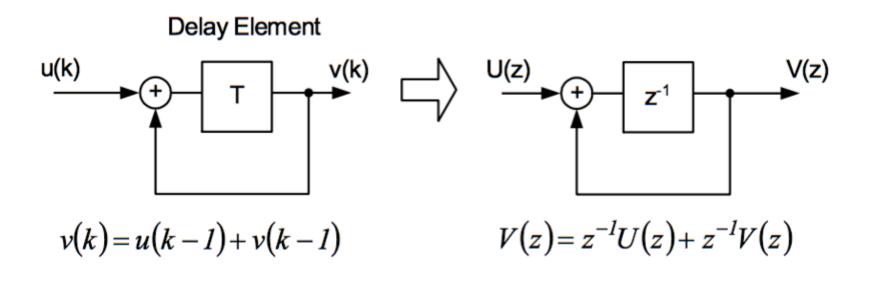
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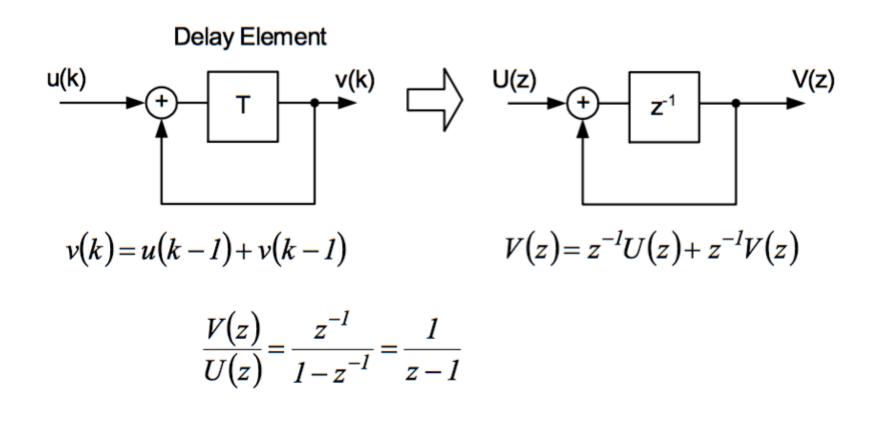
- Objective
 - Want to make STF unity in the signal frequency band
 - Want to make NTF "small" in the signal frequency band
- □ If the frequency band of interest is around DC $(0...f_B)$ we achieve this by making |A(z)| >> 1 at low frequencies
 - Means that NTF << 1
 - Means that $STF \cong 1$



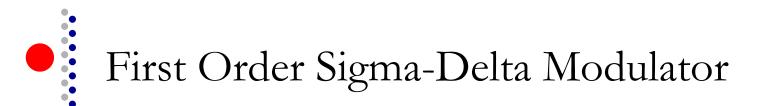


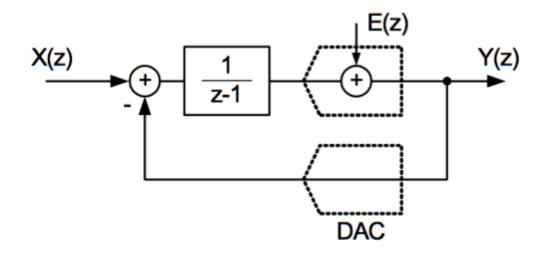
Property	Time Domain	z-Domain	ROC
Notation:	x(n)	X(z)	ROC: $r_2 < z < r_1$
	$x_1(n)$	$X_1(z)$	ROC1
	$x_2(n)$	$X_2(z)$	ROC ₂
Linearity:	$a_1x_1(n) + a_2x_2(n)$	$a_1X_1(z) + a_2X_2(z)$	At least ROC1∩ ROC2
Time shifting:	$\times (n-k)$	$z^{-k}X(z)$	At least ROC, except
			z=0 (if k>0)
	17 . 11	and the second sec	and $z = \infty$ (if $k < 0$)
z-Scaling:	$a^n \times (n)$	$X(a^{-1}z)$	$ a r_2 < z < a r_1$
Time reversal	×(-n)	$X(z^{-1})$	$\frac{1}{r_1} < z < \frac{1}{r_2}$
Conjugation:	x*(n)	$X^{*}(z^{*})$	ROC
z-Differentiation:	n x(n)	$-z \frac{dX(z)}{dz}$	$r_2 < z < r_1$
Convolution:	$x_1(n) * x_2(n)$	$X_1(z)X_2(z)$	At least ROC1∩ ROC2





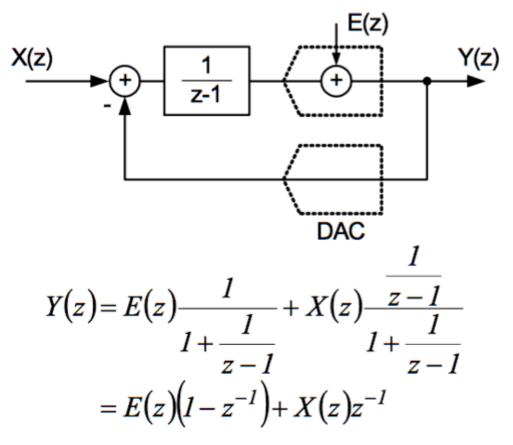
□ "Infinite gain" at DC (ω =0, z=1)





Output is equal to delayed input plus filtered quantization noise

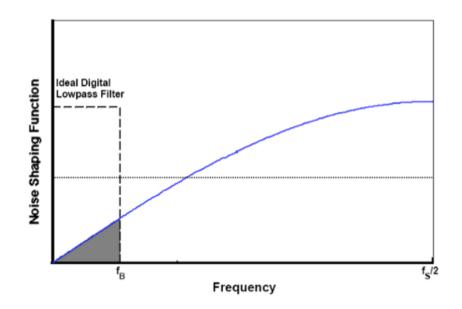




Output is equal to delayed input plus filtered quantization noise

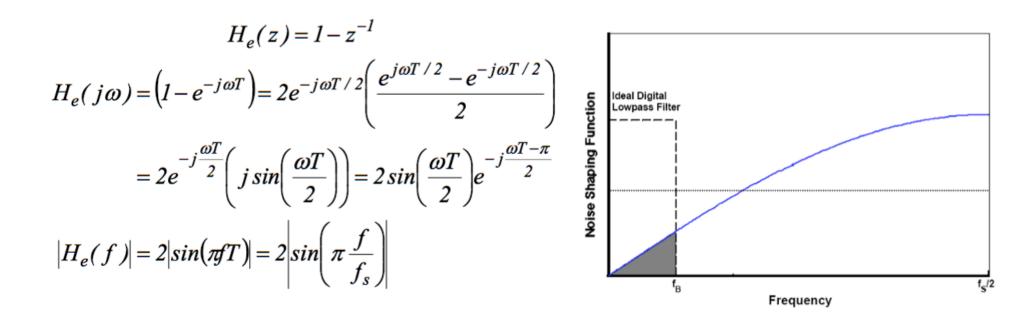


 $H_e(z) = l - z^{-l}$



- "First order noise Shaping"
 - Quantization noise is attenuated at low frequencies, amplified at high frequencies



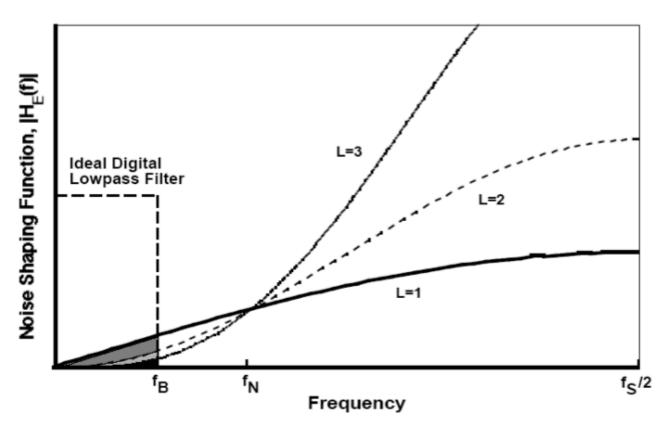


- "First order noise Shaping"
 - Quantization noise is attenuated at low frequencies, amplified at high frequencies



□ Lth order noise transfer function

$$H_E(z) = \left(l - z^{-l}\right)^L$$





- Example Revisited
 - Want 16-bit ADC, f_B=1MHz
 - Use oversampled 8-bit ADC, first order noise shaping and (ideal) digital lowpass filter

SQNR Improvement

- Example Revisited
 - Want 16-bit ADC, f_B=1MHz
 - Use oversampled 8-bit ADC, first order noise shaping and (ideal) digital lowpass filter
 - SQNR improvement compared to case without oversampling is -5.2dB+30log(M)
 - 8-bit increase in resolution (48 dB SQNR improvement) would necessitate M \cong 60 \rightarrow f_s=120MHz

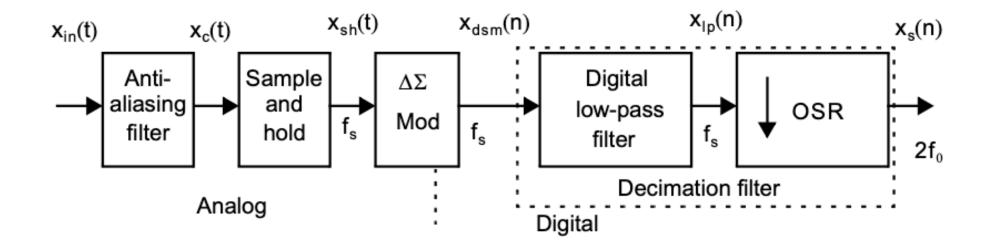
• Not all that bad!

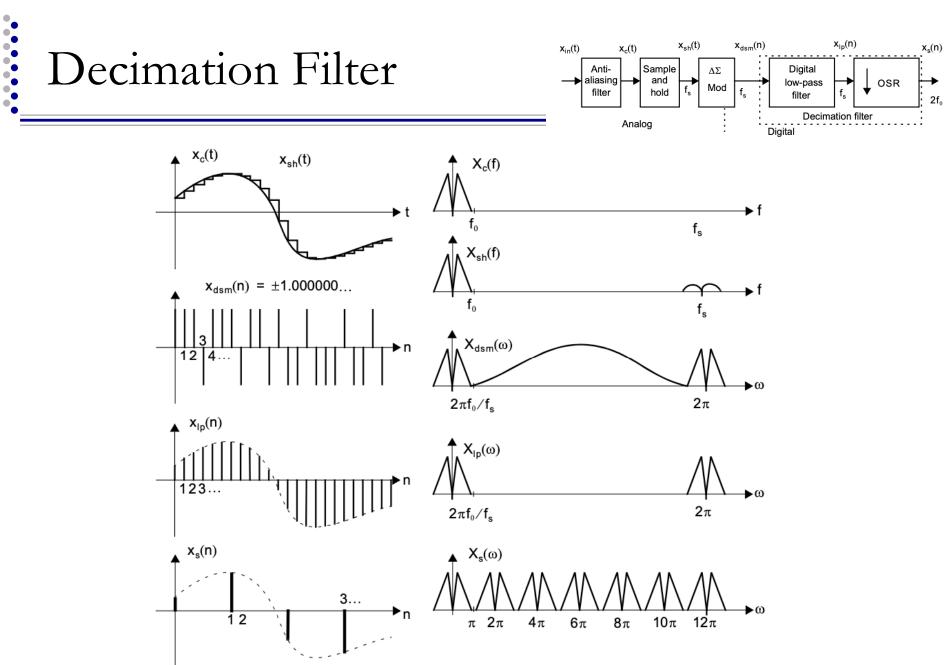


- Example Revisited
 - Want 16-bit ADC, f_B=1MHz
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 - 8-bit increase in resolution (48 dB SQNR improvement) would necessitate M \cong 60 \rightarrow f_s=120MHz
- Not all that bad!

м	SQNR improvement
16	31dB (~5 bits)
256 67dB (~11 bits)	
1024	85dB (~14 bits)







Frequency

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Time



- Oversampling
 - Enables reduction in quantization noise with digital filter
- Noise Shaping
 - Allows for further quantization noise reduction for a more practical oversampling factor
- Sigma-Delta ADCs
 - Use integrator in feedback to shape noise and achieve high resolution
 - Usually for low speed, low power applications
 - Suited for medical devices



- □ Lab tomorrow
 - DSP in Python