

Jennifer A. Gillenwater

4423 Larchwood Ave
Philadelphia, PA 19104

jengi@cis.upenn.edu
(410) 790-3763

- EDUCATION** **University of Pennsylvania**, Philadelphia, PA
Ph.D. in Computer Science expected 2013. GPA: 3.91
Rice University, Houston, TX
B.S. in Electrical Engineering, Magna Cum Laude, May 2008. GPA: 3.91
Hong Kong University of Science and Technology, Hong Kong, China
Study abroad, Spring 2007. GPA: 4.27
- EXPERIENCE** **Summer Intern—Research**: June 2010 - September 2010
Microsoft Research, Redmond, WA
Proposed and implemented a method for supervised learning of dependency parsers from relevance judgements, for re-ranking documents retrieved for long search queries.
Teaching Assistant: Fall 2009 - Fall 2010
University of Pennsylvania, Philadelphia, PA
Aided with *Machine Learning* and *Intro to Artificial Intelligence* by grading, holding office hours, teaching recitation sessions, and writing exam and homework questions.
Summer Intern—Software Testing: May 2008 - August 2008
Microsoft, Redmond, WA
Analyzed performance bottlenecks in the conversion of Word, PowerPoint, Excel, and other document types to HTML as a part of the Office Live Workspaces team.
Teaching Assistant: Spring & Fall 2006, Fall 2007
Rice University, Houston, TX
Aided with *Principles of Computing and Programming*, *Discrete Math*, and *Digital Logic Design* by holding office hours, writing quiz questions, instructing labs, and grading.
Summer Intern—Computer Science: June 2007 - August 2007
USC/ISI, Los Angeles, CA
Explored new probability-based methods of incorporating context into syntax-based translation rules for a statistical machine translation system.
Summer Intern—Solid State Physics: June 2006 - August 2006
University of Tokyo, Kashiwa, Japan
Characterized a non-contact AFM system for use in medium vacuum conditions. Conducted experiments attempting to deposit gold nanowires with the AFM cantilever.
Undergraduate Researcher: Spring 2006
Rice University, Houston, TX
Studied Verilog and developed a representative subset to serve as a basis for Featherweight Verilog, a project to improve synthesizability verification of HDL programs.
- SKILLS** **Programming experience in Java, C#, C/C++, Perl, Python, Scheme, and MATLAB.**
- ACTIVITIES** **CLUNCH, Computational Linguistics Lunch**: Organizer 2009-2010 academic year
Rice CSters, Women in Computer Science Club: Secretary 2006, Treasurer 2007
- HONORS** **NSF Graduate Research Fellowship**: Awarded Spring 2008
NSF IGERT Traineeship in Language Sciences: Awarded Spring 2008