## Formal Modeling and Analysis of Stream Processing Systems

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# Highly optimized systems



Clients on Internet







Methods discussed here are applicable to general real-time embedded systems

## Inside the box...



## A complete system



#### **Target Platform**

#### **1.** Streaming application tasks



# 2. Heterogeneous computing and memory resources



Image Coprocessor DSP		
RISC FPGA		
CAN Interface		

# 3. Heterogeneous RTOS scheduling and synchronization protocols



#### **4.** Heterogeneous communication resources





- Topology (ring, mesh, star)
- Switching strategies (packet, circuit)
- Routing strategies
  - (static, dynamic, reconfigurable)
- Arbitration policies (dynamic, TDM, CDMA)

## **The Design Problem**



## **The Design Process**



## **The Performance Analysis Problem**



### e.g. Architecture of a Picture-in-Picture App.





Maximum fill-level (backlog) of the buffers?

- Maximum end-to-end delay of the stream?
- Characteristics of the output stream?
- Characteristics of the remaining resource?

## **Key Challenges: Complex Event Streams**



- Infinite sequence of items (events)
- Highly bursty
- Events of multiple types interleaving
- Varied memory and execution demands
- Historically dependent or dynamically controlled

## Key Challenges: Complex Tasks & Architectures

- Complex processing semantics
  - fill-level of the buffers
  - execution of an internal automaton
  - synchronization between different streams
- Heterogeneous computing and communication resources
- Various scheduling policies
  - EDF, Fixed-Priority, TDMA, etc.
  - complex state-dependent scheduling schemes

#### **Complex Trade-offs**



#### **Two Categories of Performance Analysis**













## **Formal Models and Analysis Methods**

## **1** Standard Event Models (SEM)

- periodic, periodic with jitter/burst and variations
- Simple, easy to analyze
- Unrealistic assumptions
- **Too restrictive**
- Overly pessimistic results

## **2** Real-Time Calculus (RTC)

- streams & resources: count-based abstraction
- analysis: (min,+) algebra

# Capture burstiness of streams & resource availability

- Highly efficient
- Cannot model state-dependencies

# **3** Timed Automata

- model streams at very detailed level

- capture exact arrival time of each event
- Models state-dependencies
- Highly accurate
- Too detailed -> large models
- $\bigcirc$  Large systems  $\rightarrow$  inefficient

## **4** Event Count Automata (ECA)

- syntax: similar to Timed Automata
- semantics: count-based abstraction
  - capture #events in an interval of time
- Models state-dependencies
  Highly accurate
  Large systems inefficient

## **5** Hybrid Models and Methods

- RTC + SEM
- RTC + ECA
- Multi-Mode RTC

## Good accuracy-efficiency trade-off

### The rest of the talk...

# Formal Analysis using Real-Time Calculus (RTC)

## **RTC Background**

- Originated from Network Calculus in computer networks domain
  - extended for real-time embedded systems
- Worst-case deterministic formal analysis
  - variant of classical queuing theory
- Abstract models: count-based abstraction
- Analysis: min-plus / max-plus algebra

#### Recall...



Performance Model



#### **RTC Performance Model**



## **An Arrival Pattern**



#events that arrive in [t, t+ $\Delta$ ) is:  $R(t+\Delta) - R(t)$ 

## **Count-based Abstraction**

#### sliding window size

Δ	Lower bound	Upper bound
1	1	4
2	3	6



concrete time instant

## Load Model: Arrival Functions $\alpha = (\alpha^{I}, \alpha^{u})$



An arrival pattern R(t) satisfies  $\alpha$  iff  $\alpha^{I}(\Delta) \leq R(t+\Delta) - R(t) \leq \alpha^{u}(\Delta)$ 

## **A Service Pattern**



#events that can be processed in [t, t+ $\Delta$ ) is:  $C(t+\Delta) - C(t)$ 

## Service Model: Service Functions $\beta = (\beta^{I}, \beta^{u})$



A service pattern C(t) satisfies  $\beta$  iff  $\beta^{I}(\Delta) \leq C(t+\Delta) - C(t) \leq \beta^{u}(\Delta)$ 

## **Units of Arrival and Service Functions**

•  $[R(t), \alpha(\Delta)]$  and  $[C(t), \beta(\Delta)]$  can also be specified in terms of the number of resource units

– processor cycles, transmiting bit, etc.

Should *always* convert to the same unit before performing analysis