University of California at Berkeley
Department of Electrical Engineering and Computer Science
Reconfigurable Computing Seminar

CS294-7, Spring 1997 — Background Questionnaire — Tuesday, January 21

Please answer the following questions to the best of your knowledge. This will help us plan the course.

1. **General Information.**

   (a) Name:

   (b) Email address:

   (c) Your status and year (e.g. G2):

   (d) Your degree goal:

   (e) Select one: **credit** **audit**

   (f) Current research project (if you have one):

   (g) Other technical and research interests:

   (h) Name of research advisor (if you have one):

   (i) Do you have access to a workstation/PC? (which systems?)

   (j) Timeslot preference for this course

Time	8-9:30AM	9:30-11AM	11-12:30PM	12:30-2PM	2-3:30PM
Rank			CS250		

Rank preference (1-most preferred...4-least); Mark an 'X' through times when you could not make the class.

(k) Other courses you are taking this semester:

(l) Why are you interested in this particular course?

(m) What do you expect to learn from the course?

(n) Have you taken CS250?

When and from whom?

If not, have you taken an equivalent VLSI course?

Where? From whom? What book?

(o) Have you taken CS252?

When and from whom?

If not, have you taken an equivalent architecture course?

Where? From whom? What book?

(p) Have you taken EE141/241?

When and from whom?

(q) Have you taken EE219b/244?

When and from whom?

2. **FPGA Toolset experience.** For each of the following tool types indicate your level of experience as "none, some, or much"; *Please name the tool that you used*:

   (a) High-level Design Languages (*e.g.* VHDL, Verilog):

   (b) Logic-Synthesis (*e.g.* Synopsys):

   (c) Schematic Capture (*e.g.* Viewlogic):

   (d) Back-end P&R (*e.g.* XACT):

   (e) Others?

3. Describe your most ambitious FPGA project:

4. Describe your most ambitious hardware or IC project:

5. Describe your most ambitious software project: