

ESE 531: Digital Signal Processing

Lec 12: February 21st, 2017
Data Converters, Noise Shaping (con't)



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Lecture Outline

- Data Converters
 - Anti-aliasing
 - ADC
 - Quantization
 - Practical DAC
- Noise Shaping

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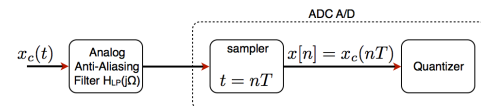
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ADC



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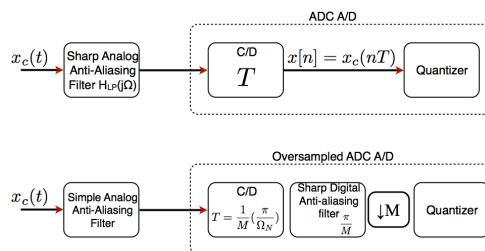
Anti-Aliasing Filter with ADC



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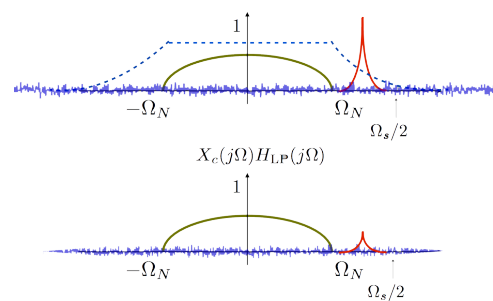
Oversampled ADC



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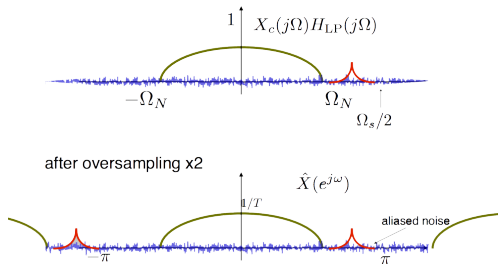
Oversampled ADC



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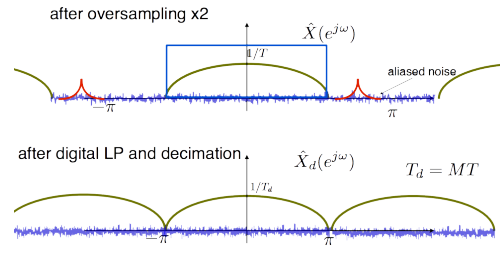
Oversampled ADC



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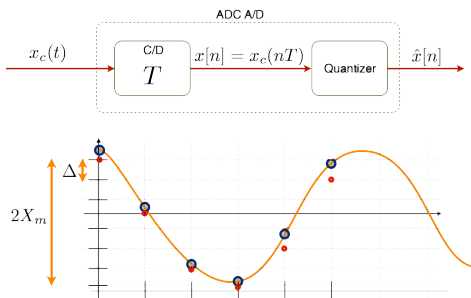
Oversampled ADC



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Sampling and Quantization



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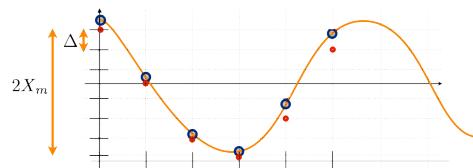
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Sampling and Quantization

for 2's complement with B+1 bits $-1 \leq \hat{x}_B[n] < 1$

$$\Delta = \frac{2X_m}{2^{B+1}} = \frac{X_m}{2^B}$$

$$\hat{x}[n] = X_m \hat{x}_B[n]$$



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Effect of Quantization Error on Signal

- Quantization error is a deterministic function of the signal
 - Consequently, the effect of quantization strongly depends on the signal itself
- Unless, we consider fairly trivial signals, a deterministic analysis is usually impractical
 - More common to look at errors from a statistical perspective
 - "Quantization noise"
- Two aspects
 - How much noise power (variance) does quantization add to our samples?
 - How is this noise distributed in frequency?

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Quantization Error



- Model quantization error as noise

$$\hat{x}[n] = x[n] + e[n]$$

- In that case:

$$-\Delta/2 \leq e[n] < \Delta/2$$

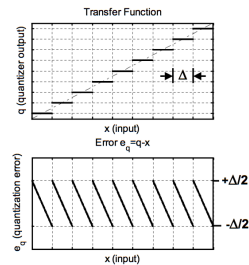
$$(-X_m - \Delta/2) < x[n] \leq (X_m - \Delta/2)$$

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Ideal Quantizer

- Quantization step Δ
- Quantization error has sawtooth shape,
- Bounded by $-\Delta/2, +\Delta/2$
- Ideally infinite input range and infinite number of quantization levels

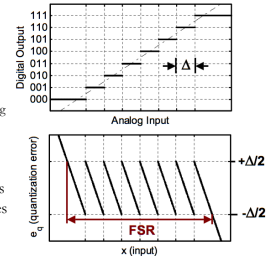


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EE315B, Stanford

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Ideal B-bit Quantizer

- Practical quantizers have a limited input range and a finite set of output codes
- E.g. a 3-bit quantizer can map onto $2^3=8$ distinct output codes
 - Diagram on the right shows "offset-binary encoding"
 - See Gustavsson (p.2) for other coding formats
- Quantization error grows out of bounds beyond code boundaries
- We define the full scale range (FSR) as the maximum input range that satisfies $|e_q| \leq \Delta/2$
 - Implies that $FSR = 2^B \cdot \Delta$

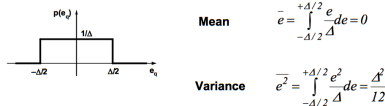


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Quantization Error Statistics

- Crude assumption: $e_q(x)$ has uniform probability density
- This approximation holds reasonably well in practice when
 - Signal spans large number of quantization steps
 - Signal is "sufficiently active"
 - Quantizer does not overload



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Noise Model for Quantization Error

- Assumptions:
 - Model $e[n]$ as a sample sequence of a stationary random process
 - $e[n]$ is not correlated with $x[n]$
 - $e[n]$ not correlated with $e[m]$ where $m \neq n$ (white noise)
 - $e[n] \sim U[-\Delta/2, \Delta/2]$ (uniform pdf)
- Result:
- Variance is: $\sigma_e^2 = \frac{\Delta^2}{12}$, or $\sigma_e^2 = \frac{2^{-2B} X_m^2}{12}$ since $\Delta = 2^{-B} X_m$
- Assumptions work well for signals that change rapidly, are not clipped, and for small Δ

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Signal-to-Quantization-Noise Ratio

- For uniform B+1 bits quantizer

$$\begin{aligned} SNR_Q &= 10 \log_{10} \left(\frac{\sigma_x^2}{\sigma_e^2} \right) \\ &= 10 \log_{10} \left(\frac{12 \cdot 2^{2B} \sigma_x^2}{X_m^2} \right) \end{aligned}$$

$$SNR_Q = 6.02B + 10.8 - 20 \log_{10} \left(\frac{X_m}{\sigma_x} \right) \text{Quantizer range rms of amp}$$

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Signal-to-Quantization-Noise Ratio

$$SNR_Q = 6.02B + 10.8 - 20 \log_{10} \left(\frac{X_m}{\sigma_x} \right) \text{Quantizer range rms of amp}$$

- Improvement of 6dB with every bit
- The range of the quantization must be adapted to the rms amplitude of the signal
 - Tradeoff between clipping and noise!
 - Often use pre-amp
 - Sometimes use analog auto gain controller (AGC)

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Signal-to-Quantization-Noise Ratio

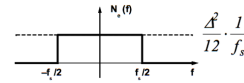
- Assuming full-scale sinusoidal input, we have

$$\text{SQNR} = \frac{P_{\text{sig}}}{P_{\text{qnoise}}} = \frac{1}{2} \left(\frac{2^B \Delta}{2} \right)^2 = 1.5 \times 2^{2B} = 6.02B + 1.76 \text{ dB}$$

B (Number of Bits)	SQNR
8	50dB
12	74dB
16	98dB
20	122dB

Quantization Noise Spectrum

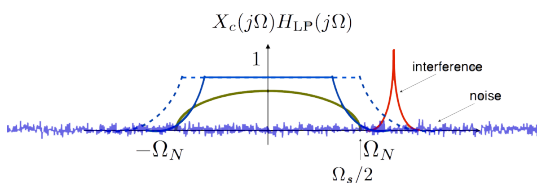
- If the quantization error is "sufficiently random", it also follows that the noise power is uniformly distributed in frequency



References

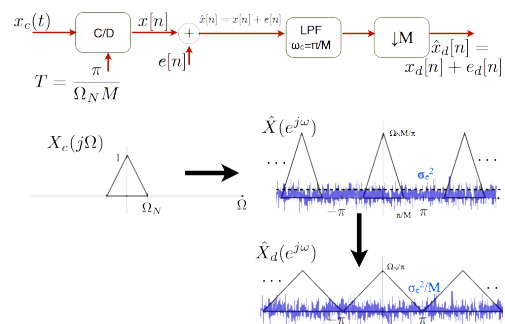
- W. R. Bennett, "Spectra of quantized signals," Bell Syst. Tech. J., pp. 446-72, July 1988.
- B. Widrow, "A study of rough amplitude quantization by means of Nyquist sampling theory," IRE Trans. Circuit Theory, vol. CT-3, pp. 266-76, 1956.

Non-Ideal Anti-Aliasing Filter



- Problem: Hard to implement sharp analog filter
- Solution: Crop part of the signal and suffer from noise and interference

Quantization Noise with Oversampling



Quantization Noise with Oversampling

- Energy of $x_d[n]$ equals energy of $x[n]$
 - No filtering of signal!
- Noise variance is reduced by factor of M

$$\text{SNR}_Q = 6.02B + 10.8 - 20 \log_{10} \left(\frac{X_m}{\sigma_x} \right) + 10 \log_{10} M$$

- For doubling of M we get 3dB improvement, which is the same as 1/2 a bit of accuracy
 - With oversampling of 16 with 8bit ADC we get the same quantization noise as 10bit ADC!

Practical DAC

Practical DAC

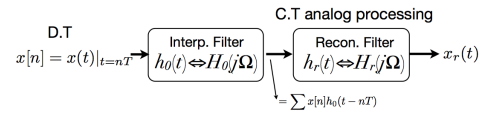
$$\text{D.T. } x[n] = x(t)|_{t=nT} \rightarrow \boxed{\text{sinc pulse generator}} \rightarrow x_r(t) = \sum_{n=-\infty}^{\infty} x[n] \text{C.T. } \text{sinc}\left(\frac{t-nT}{T}\right)$$

- Scaled train of sinc pulses
- Difficult to generate sinc \rightarrow Too long!

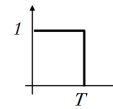
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Practical DAC



- $h_0(t)$ is finite length pulse \rightarrow easy to implement
- For example: zero-order hold



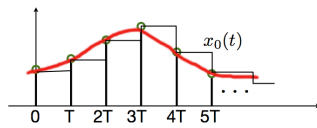
$$H_0(j\Omega) = T e^{-j\Omega \frac{T}{2}} \text{sinc}\left(\frac{\Omega}{\Omega_s}\right)$$

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Practical DAC

Zero-Order-Hold interpolation



$$x_0(t) = \sum_{n=-\infty}^{\infty} x[n] h_0(t - nT) = h_0(t) * x_s(t)$$

Taking a FT:

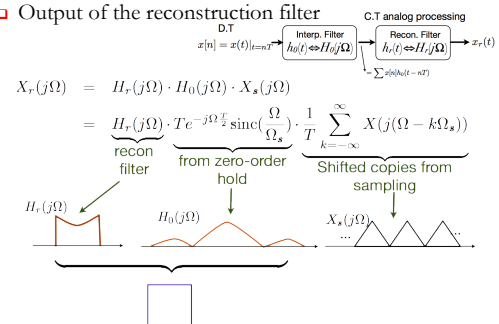
$$\begin{aligned} X(j\Omega) &= H_0(j\Omega) X_s(j\Omega) \\ &= H_0(j\Omega) \frac{1}{T} \sum_{k=-\infty}^{\infty} X(j(\Omega - k\Omega_s)) \end{aligned}$$

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Practical DAC

- Output of the reconstruction filter



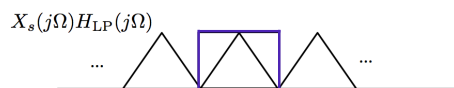
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Practical DAC



Ideally:



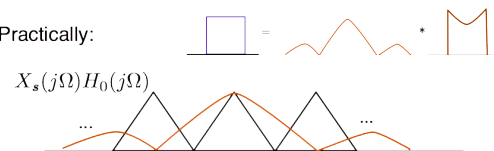
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Practical DAC



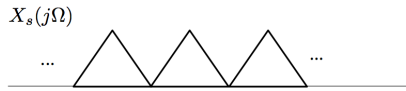
Practically:



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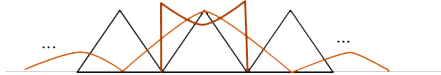
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Practical DAC

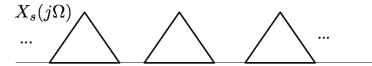
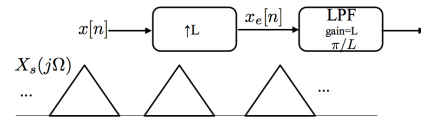


Practically:

$$X_s(j\Omega)H_0(j\Omega)H_r(j\Omega)$$

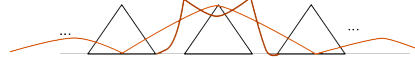


Practical DAC with Upsampling



Practically:

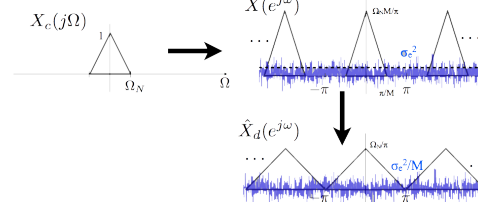
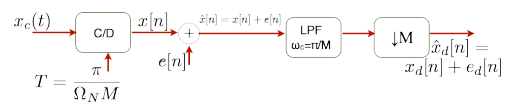
$$X_s(j\Omega)H_0(j\Omega)H_r(j\Omega)$$



Noise Shaping



Quantization Noise with Oversampling



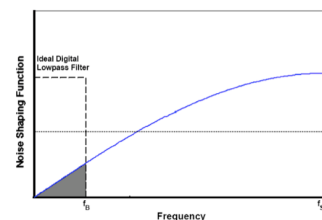
Quantization Noise with Oversampling

- Energy of $x_d[n]$ equals energy of $x[n]$
 - No filtering of signal!
- Noise variance is reduced by factor of M

$$\text{SNR}_Q = 6.02B + 10.8 - 20 \log_{10} \left(\frac{X_m}{\sigma_x} \right) + \underbrace{10 \log_{10} M}_{\text{Oversampling Gain}}$$

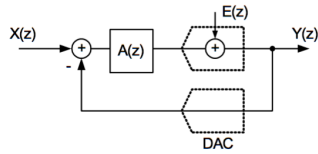
- For doubling of M we get 3dB improvement, which is the same as 1/2 a bit of accuracy
 - With oversampling of 16 with 8bit ADC we get the same quantization noise as 10bit ADC!

Noise Shaping



- Idea: "Somehow" build an ADC that has most of its quantization noise at high frequencies
- Key: Feedback

Noise Shaping Using Feedback



$$\begin{aligned} Y(z) &= E(z) + A(z)X(z) - A(z)Y(z) \\ &= E(z) \frac{1}{1+A(z)} + X(z) \frac{A(z)}{1+A(z)} \\ &= E(z) \underbrace{H_E(z)}_{\text{Noise Transfer Function}} + X(z) \underbrace{H_X(z)}_{\text{Signal Transfer Function}} \end{aligned}$$

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Noise Shaping Using Feedback

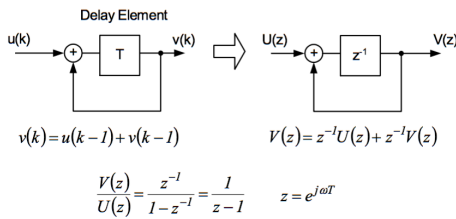
$$Y(z) = E(z) \underbrace{\frac{1}{1+A(z)}}_{\text{Noise Transfer Function}} + X(z) \underbrace{\frac{A(z)}{1+A(z)}}_{\text{Signal Transfer Function}}$$

- Objective
 - Want to make STF unity in the signal frequency band
 - Want to make NTF "small" in the signal frequency band
- If the frequency band of interest is around DC ($0 \dots f_B$) we achieve this by making $|A(z)| \gg 1$ at low frequencies
 - Means that NTF $\ll 1$
 - Means that STF ≈ 1

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Discrete Time Integrator

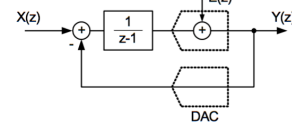


- "Infinite gain" at DC ($\omega=0, z=1$)

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First Order Sigma-Delta Modulator



$$\begin{aligned} Y(z) &= E(z) \frac{1}{1 + \frac{1}{z-1}} + X(z) \frac{\frac{z-1}{z-1}}{1 + \frac{1}{z-1}} \\ &= E(z) (1 - z^{-1}) + X(z) z^{-1} \end{aligned}$$

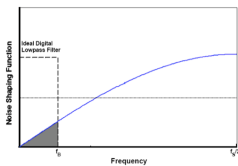
- Output is equal to delayed input plus filtered quantization noise

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NTF Frequency Domain Analysis

$$\begin{aligned} H_e(z) &= 1 - z^{-1} \\ H_e(j\omega) &= (1 - e^{-j\omega T}) = 2e^{-j\omega T/2} \left(\frac{e^{j\omega T/2} - e^{-j\omega T/2}}{2} \right) \\ &= 2e^{-j\omega T/2} \left(j \sin\left(\frac{\omega T}{2}\right) \right) = 2 \sin\left(\frac{\omega T}{2}\right) e^{-j\omega T/2 - \pi/2} \\ |H_e(f)| &= 2 \left| \sin\left(\frac{\omega T}{2}\right) \right| = 2 \left| \sin\left(\pi \frac{f}{f_s}\right) \right| \end{aligned}$$



- "First order noise Shaping"
 - Quantization noise is attenuated at low frequencies, amplified at high frequencies

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In-Band Quantization Noise

- Question: If we had an ideal digital lowpass, what is the achieved SQNR as a function of oversampling ratio?
- Can integrate shaped quantization noise spectrum up to f_B and compare to full-scale signal

$$\begin{aligned} P_{qnoise} &= \int_0^{f_B} \frac{\Delta^2}{12} \cdot \frac{2}{f_s} \cdot \left[2 \sin\left(\pi \frac{f}{f_s}\right) \right]^2 df \\ &\approx \int_0^{f_B} \frac{\Delta^2}{12} \cdot \frac{2}{f_s} \cdot \left[2\pi \frac{f}{f_s} \right]^2 df \\ &\approx \frac{\Delta^2}{12} \cdot \frac{\pi^2}{3} \left[\frac{2f_B}{f_s} \right]^3 = \frac{\Delta^2}{12} \cdot \frac{\pi^2}{3} \frac{1}{M^3} \end{aligned}$$

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In-Band Quantization Noise

- Assuming a full-scale sinusoidal signal, we have

$$SQNR \cong \frac{P_{sig}}{P_{noise}} = \frac{1 \left(\frac{2^B - 1}{2} \Delta \right)^2}{\frac{\Delta^2}{12} \cdot \frac{\pi^2}{3} \cdot \frac{1}{M^3}} = 1.5 \times (2^B - 1)^2 \times \underbrace{\frac{3}{\pi^2} \times M^3}_{\text{Due to noise shaping \& digital filter}}$$

$$\cong 1.76 + 6.02B - 5.2 + 30 \log(M) \quad [dB] \quad (\text{for large } B)$$

- Each 2x increase in M results in 8x SQNR improvement
 - Also added 1/2 bit resolution

Digital Noise Filter

- Increasing M by 2x, means 3-dB reduction in quantization noise power, and thus 1/2 bit increase in resolution
 - "1/2 bit per octave"
- Is this useful?
- Reality check
 - Want 16-bit ADC, $f_B = 1\text{MHz}$
 - Use oversampled 8-bit ADC with digital lowpass filter
 - 8-bit increase in resolution necessitates oversampling by 16 octaves

$$f_s \geq 2 \cdot f_B \cdot M = 2 \cdot 1\text{MHz} \cdot 2^{16}$$

$$\geq 131\text{GHz}$$

SQNR Improvement

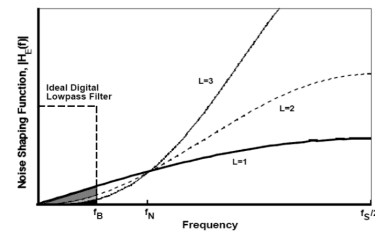
- Example Revisited
 - Want 16-bit ADC, $f_B = 1\text{MHz}$
 - Use oversampled 8-bit ADC, first order noise shaping and (ideal) digital lowpass filter
 - SQNR improvement compared to case without oversampling is -5.2dB + 30log(M)
 - 8-bit increase in resolution (48 dB SQNR improvement) would necessitate $M=60 \rightarrow f_s = 120\text{MHz}$
- Not all that bad!

M	SQNR improvement
16	31dB (~5 bits)
256	67dB (~11 bits)
1024	85dB (~14 bits)

Higher Order Noise Shaping

- L^{th} order noise transfer function

$$H_E(z) = (1 - z^{-1})^L$$



Big Ideas

- Data Converters
 - Oversampling to reduce interference and quantization noise \rightarrow increase ENOB (effective number of bits)
 - Practical DACs use practical interpolation and reconstruction filters with oversampling
- Noise Shaping
 - Use feedback to reduce oversampling factor

Admin

- HW 4 due tonight at midnight
 - Typo in code in MATLAB problem, corrected handout
 - See Piazza for more information
- HW 5 posted after class
 - Due in 1.5 weeks 3/3