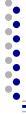
ESE 531: Digital Signal Processing

Week 1

Lecture 1: January 20, 2021

Introduction and Overview





Lecture Outline

- Course Topics Overview
- Learning Objectives
- Course Structure
- Course Policies
- Course Content
- □ What is DSP?
- DSP Examples

Course Topics Overview

- Discrete-Time (DT) Signals
- □ Time-Domain Analysis of DT Systems
- Discrete Fourier Transform (DFT)
- Fast Fourier Transform (FFT)
- Discrete-Time Fourier Transform (DTFT)
- □ z-Transform
- Sampling of Continuous Time Signals
- Data Converters and Modulation
- Upsampling/Downsampling
- Discrete-Time Filter Design
- Special Topics



- □ Learn the fundamentals of digital signal processing
- Provide an understanding of discrete-time signals and systems and digital filters
- Enable you to apply DSP concepts to a wide range of fields
- Gain the ability to read the technical literature on DSP
- Apply the techniques learned in a two projects focused on filter design



Learning Objectives

□ In other words...

□ Math, Math, Math*

*With MATLAB application for intuition

Course Structure

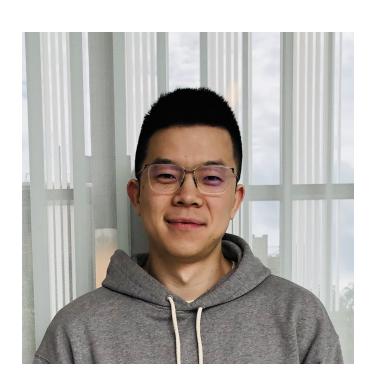
- Week's worth of material released on Sunday
 - 2-3 lecture videos in Canvas
 - Slides available on course webpage calendar
 - Assignment (HW or project) due the following Sunday
 - See course calendar for full schedule
- □ Website (http://www.seas.upenn.edu/~ese531/)
 - Course calendar is used for all handouts (lectures slides, assignments, and readings)
 - Canvas used for lecture videos, assignment submission, exams, and grades
 - Piazza used for announcements and discussions
 - Use for Zoom links for lectures and OHs

Course Structure

- Course Staff (complete info on course website)
- Instructor: Tania Khanna
 - ESE 531 Office hours T 9-10am, Th 3:30-4:30pm
 - Office hours Wednesday 1-3 pm or by appointment
 - Email: <u>taniak@seas.upenn.edu</u>
 - Best way to reach me

□ TAs:

- Shuang Wu
 - Recitation F TBD
 - Work out problems
 - recorded and uploaded into Canvas
 - Office hours TBD
- Still searching for 2nd TA



Course Structure

Lectures

- You will do better if you keep pace with the course and not binge lectures
 - Pros: Can watch pause and rewatch
 - Cons: Can't ask questions. Use office hours!

Textbook

- A. V. Oppenheim and R. W. Schafer (with J. R. Buck), Discrete-Time Signal Processing. 3rd. Edition,
 Prentice-Hall, 2010
 - Homework will be from text
- Class will follow text structure... mostly



Course Structure - Assignments/Exams

- □ Homework one week long (8 total)* [25%]
 - Due Sundays at midnight
 - Combination of book problems and matlab problems
- □ Projects two projects [35%]
 - Work individually
 - Different DSP applications of filter design
- □ Midterm exam [20%]
- □ Final exam [20%]
 - Exams administered in Canvas



Course Policies

See web page for full details

- □ Turn homework in Canvas
 - Anything handwritten/drawn must be clearly legible
 - Submit code, graphs, test results when specified
- Individual work
 - code, test simulations, analysis, writeups
 - May discuss strategies, but acknowledge help
- Late homeworks
 - 4 late days allowed
 - Can only use one max late day on projects



I want to hear from you...

- Accessibility Survey in Canvas
 - Submit by Sunday 1/24 for full HW credit
- □ Will you be in a different time zone?
- Will you have trouble seeing or hearing video lectures?
- Are there any other accessibility issues I should know about?

□ Let me know any concerns -- I will do everything I can to ensure you achieve the learning objectives

Course Content

- Introduction
- Discrete Time Signals & Systems
- Discrete Time FourierTransform
- Z-Transform
- □ Inverse Z-Transform
- Sampling of Continuous Time Signals
- Frequency Domain of DiscreteTime Series
- Downsampling/Upsampling
- Data Converters, Sigma Delta Modulation

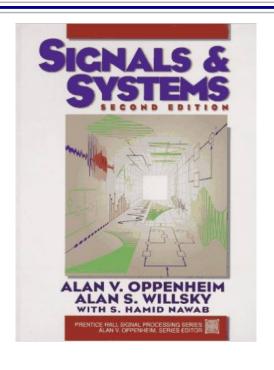
- Frequency Response of LTI Systems
- Signal Flow Representation
- Basic Structures for IIR and FIR Systems
- Design of IIR and FIR Filters
- Butterworth, Chebyshev, and Elliptic Filters
- Filter Banks
- Adaptive Filters
- Computation of the Discrete Fourier Transform
- Fast Fourier Transform

Course Content

ESE531 Spring 2021 Working Schedule

>>						
Wk Date		ate	Lecture	Slides	Due	Reading
	1/20	W	Week 1 Release: Discrete Time Signals & Systems			review course webpage completely
1	1/21	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	1/24	Su	Week 2 Release: Discrete Time Signals & Systems		Accessibility Survey	2.1-2.4
	1/26	T	Instructor Office Hours, 9-10am (Zoom Link in Piazza)			
\parallel_2	1/28	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	1/31	Su	Week 3 release: Discrete Time Fourier Transform, Z-Transform		HW 0, Diagnostics Quiz	2.5-2.7, 3.0-3.1
	2/2	T	Instructor Office Hours, 9-10am (Zoom Link in Piazza)			
3	2/4	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	2/7	Su	Week 4 release: Inverse Z-Transform, Sampling and Reconstruction		MATLAB Tutorial, HW 1	3.3, 4.0-4.3
	2/9	T	Instructor Office Hours, 9-10am (Zoom Link in Piazza)			
4	2/11	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	2/14	Su	Week 5 release: DT/CT Processing of CT/DT Signals, Re-sampling and Practical Interpolation		HW 2	4.4-4.5, 4.6-4.6.3
	2/16	T	Instructor Office Hours, 9-10am (Zoom Link in Piazza)			
5	2/18	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	2/21	Su	Week 6 release: Non-Integer and Multi-rate Sampling, Polyphase Decomposition, and Multi-rate Filter Banks		HW 3	4.6.4-4.7
	2/23	T	Instructor Office Hours, 9-10am (Zoom Link in Piazza)			
6	2/25	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	2/28	Su	Week 7 release: Data Converters, Noise Shaping, and Frequency Response of LTI Systems		HW 4	4.8-4.9, 5.0-5.4
	3/2	T	Instructor Office Hours, 9-10am (Zoom Link in Piazza)			
7	3/4	Th	Instructor Office Hours, 3:30-4:30pm (Zoom Link in Piazza)			
	2/7	G				

Signals and Systems Review





- https://www.seas.upenn.edu/~ese531/spring2021/knowled
 ge_roundup.html
- Diagnostic Quiz in Canvas
 - Complete by 1/31 for full credit
 - Review in HW 0
 - Also Matlab Tutorial

What is DSP





DSP is Everywhere

Sound applications

- Compression, enhancement, special effects, synthesis, recognition, echo cancellation,...
- Cell phones, MP3 players, movies, dictation, text-tospeech,...

Communication

- Modulation, coding, detection, equalization, echo cancellation,...
- Cell Phones, dial-up modem, DSL modem, Satellite Receiver,...

Automotive

 ABS, GPS, Active Noise Cancellation, Cruise Control, Parking, Driverless Cars...



DSP is Everywhere (con't)

- Medical
 - Magnetic Resonance, Tomography, Electrocardiogram, Biometric Monitoring...
- Military
 - Radar, Sonar, Space photographs, remote sensing,...
- Image and Video Applications
 - DVD, JPEG, Movie special effects, video conferencing...
- Mechanical
 - Motor control, process control, oil and mineral prospecting,...

Signal Processing

- Humans are the most advanced signal processors
 - speech and pattern recognition, speech synthesis,...
- We encounter many types of signals in various applications
 - Electrical signals: voltage, current, magnetic and electric fields,...
 - Mechanical signals: velocity, force, displacement,...
 - Acoustic signals: sound, vibration,...
 - Other signals: pressure, temperature, biometrics...
- Most real-world signals are analog
 - They are continuous in time and amplitude
 - Convert to voltage or currents using sensors and transducers



Signal Processing (con't)

- Analog circuits process these signals using
 - Resistors, Capacitors, Inductors, Amplifiers,...
- Analog signal processing examples
 - Audio processing in FM radios
 - High end stereo equipment
 - Video processing in traditional TV sets



Limitations of Analog Signal Processing

- Accuracy limitations due to
 - Component tolerances
 - Undesired nonlinearities
- Limited repeatability due to
 - Tolerances
 - Changes in environmental conditions
 - Temperature
 - Vibration
- Sensitivity to electrical noise
- Limited dynamic range for voltage and currents
- Inflexibility to changes
- Difficulty of implementing certain operations
 - Nonlinear operations
 - Time-varying operations
- Difficulty of storing information

Digital Signal Processing

- Represent signals by a sequence of numbers
 - Sampling and quantization (or analog-to-digital conversion)
- Perform processing on these numbers with a digital processor
 - Digital signal processing
- Reconstruct analog signal from processed numbers
 - Reconstruction or digital-to-analog conversion



- Analog input → analog output
 - Eg. Digital recording music
- Analog input → digital output
 - Eg. Touch tone phone dialing, speech to text
- Digital input → analog output
 - Eg. Text to speech
- Digital input → digital output
 - Eg. Compression of a file on computer



Pros and Cons of Digital Signal Processing

Pros

- Accuracy can be controlled by choosing word length
- Repeatable
- Sensitivity to electrical noise is minimal
- Dynamic range can be controlled using floating point numbers
- Flexibility can be achieved with software implementations
- Non-linear and time-varying operations are easier to implement
- Digital storage is cheap
- Digital information can be encrypted for security
- Price/performance and reduced time-to-market



Pros and Cons of Digital Signal Processing

□ Pros

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Cons

- Sampling causes loss of information
- A/D and D/A requires mixed-signal hardware
- Limited speed of processors
- Quantization and round-off errors

DSP Examples





Example I: Audio Compression

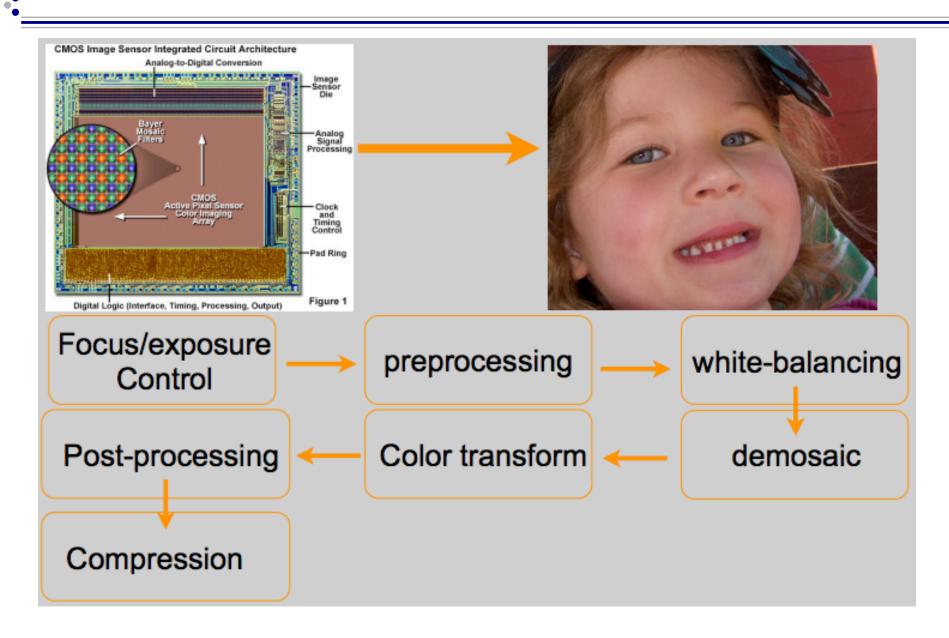
- Compress audio by 10x without perceptual loss of quality
- Sophisticated processing based on models of human perception
- 3MB files instead of 30MB
 - Entire industry changed in less than 10 years!

Historical Forms of Compression

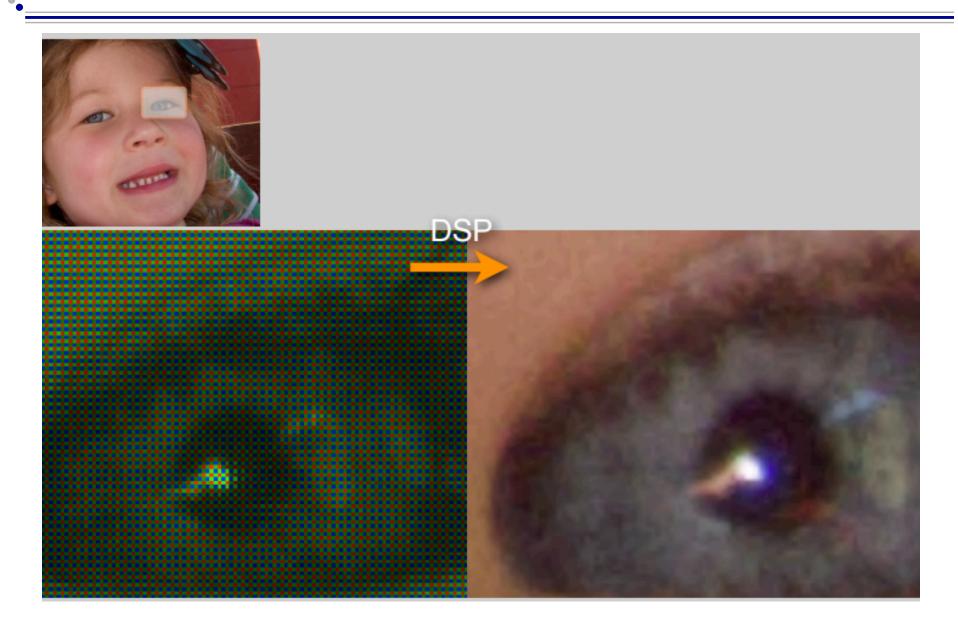
- Morse code: dots (1 unit) dashes (3 units)
 - Code Length inversely proportional to frequency of character
 - E (12.7%) = . (1 unit) Q (0.1%) = --.- (10 units)
- □ "92 Code"
 - Used by Western-Union in 1859 to reduce BW on telegraph lines by numerical codes for frequently used phrases
 - \bullet 1 = wait a minute
 - 73 = Best Regards
 - 88 = Loves and Kisses



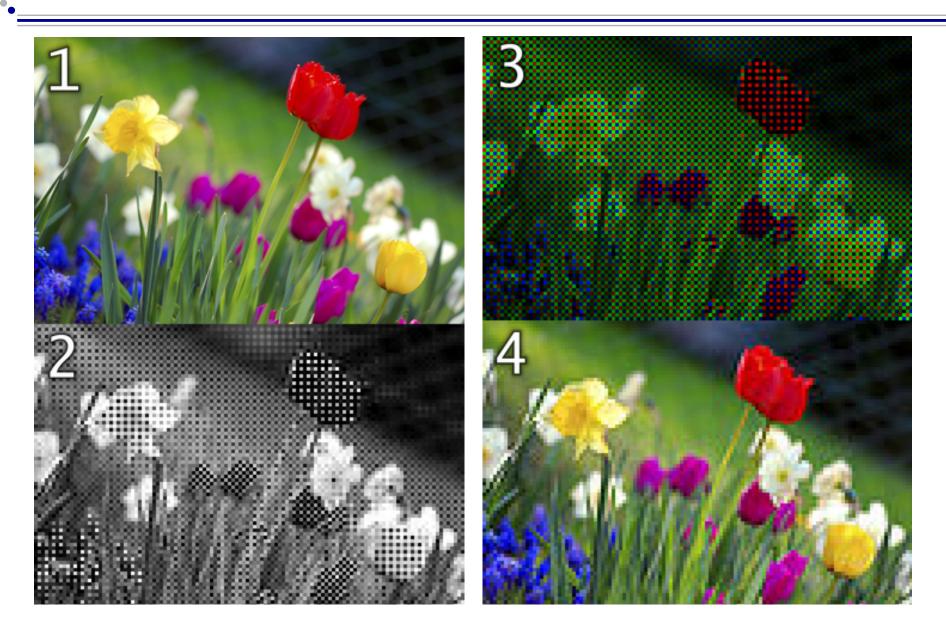
Example II: Digital Imaging Camera



Example II: Digital Imaging Camera



Example II: Digital Imaging Camera







□ Compression of 40x without perceptual loss of quality.

■ Example of slight over compression: difference enables 60x compression!

Computational Photography

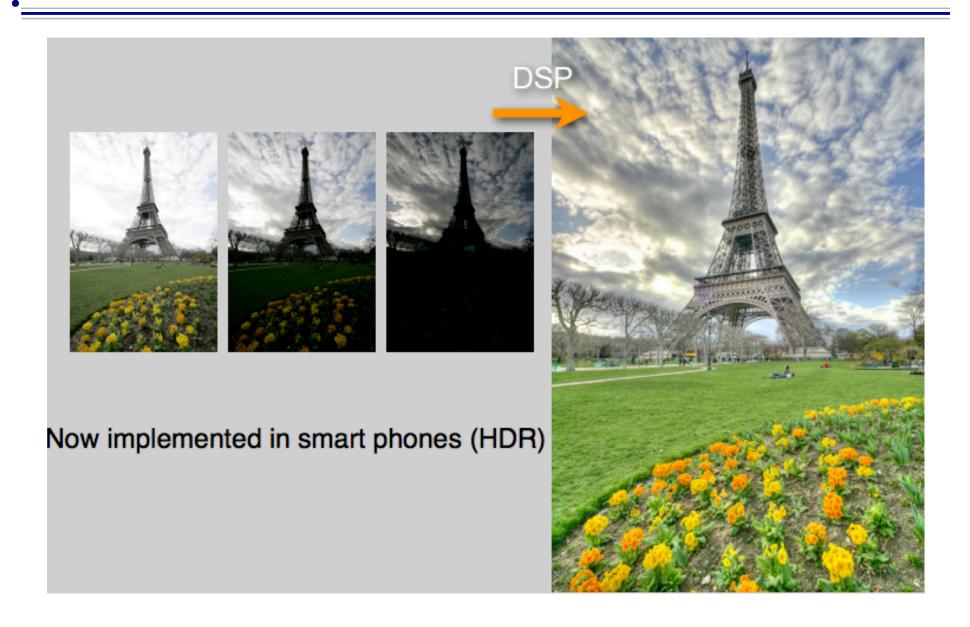


Image Processing

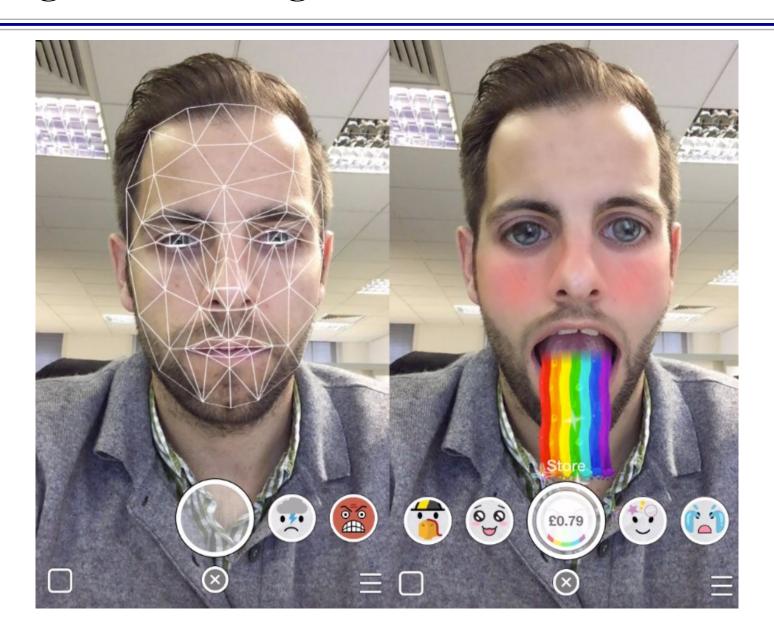


Image Processing - Saves Lives

Canadian 'swirl face' Thailand

jailed in

August 15, 2008

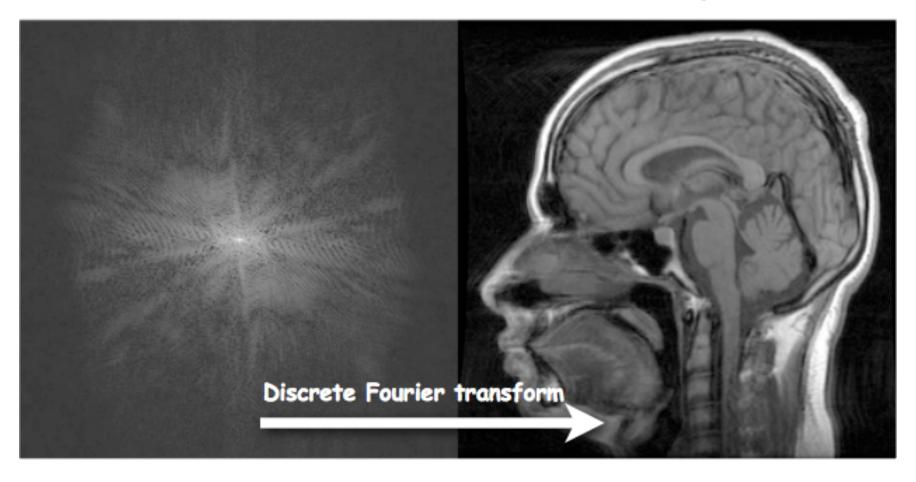


Images released by Interpol in 2007 show the 'unswirling' of the internet pictures that led to the capture of Christopher Paul Neil.

Example III: MRI

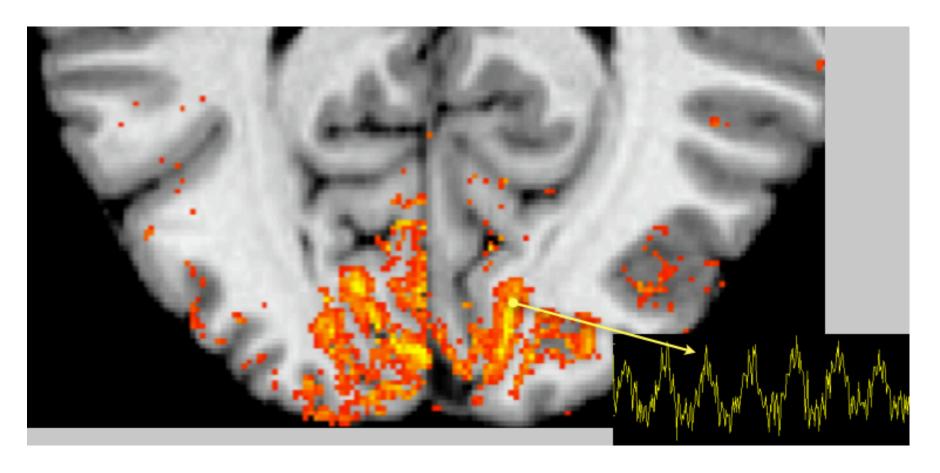
k-space (raw data)

Image



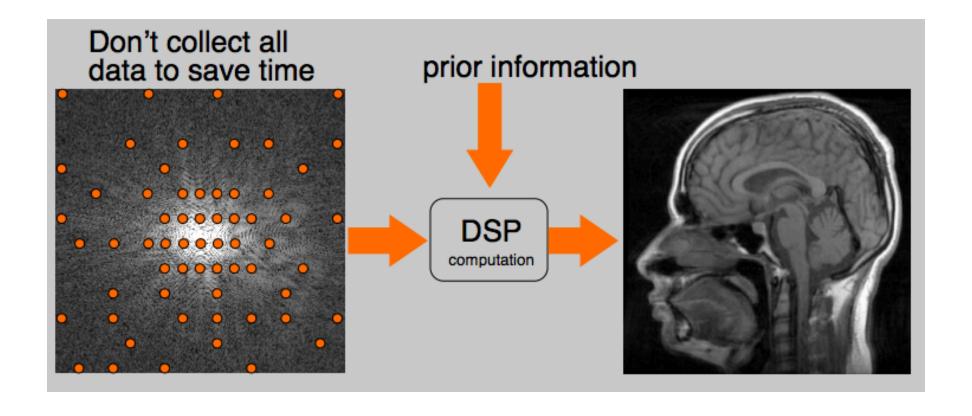
fMRI example

- Sensitivity to blood oxygenation
 - response to brain activity Convert from one signal to another

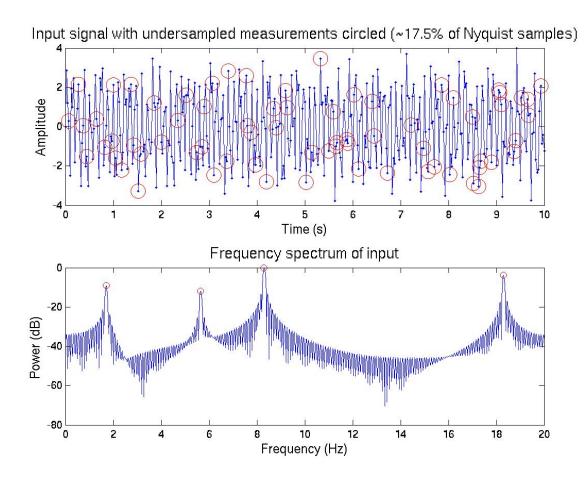


Compressive Sampling

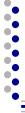
Compression meets sampling



Example: Sum of Sinusoids

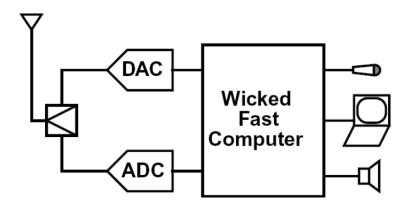


- Sense signal randomly M times
 - $M > C \cdot \mu 2(\Phi, \Psi) \cdot S \cdot \log N$
- Recover with linear program

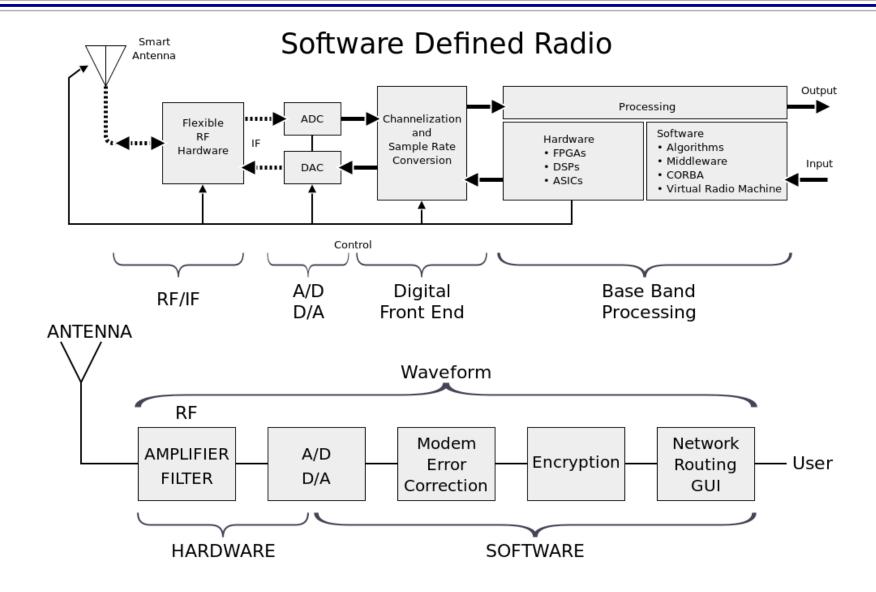


Example IV: Software Defined Radio

- □ Traditional radio:
 - Hardware receiver/mixers/demodulators/filtering
 - Outputs analog signals or digital bits
- Software Defined Radio:
 - Uses RF front end for baseband signal
 - High speed ADC digitizes samples
 - All processing chain done in software



Software Defined Radio

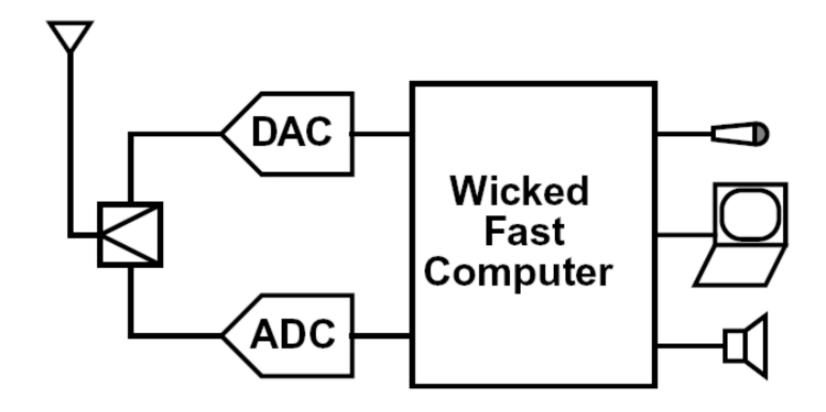




Software Defined Radio

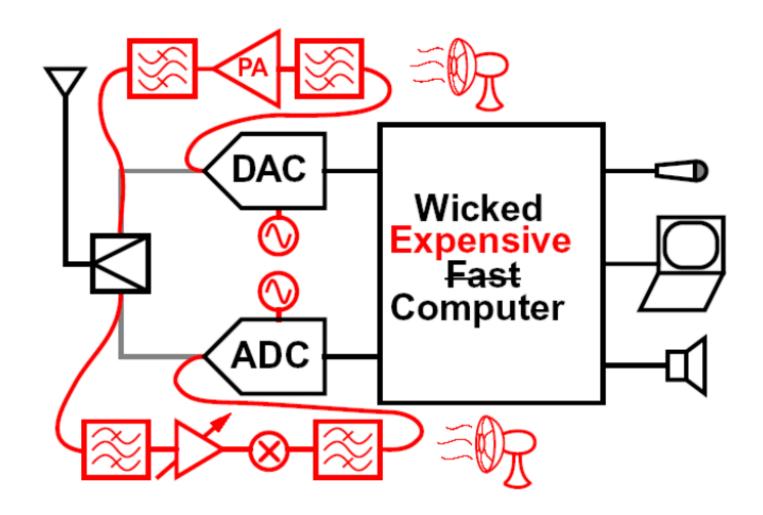
- Advantages:
 - Flexibility
 - Upgradable
 - Sophisticated processing
 - Ideal Processing chain
 - not approximate like in analog hardware
- Already used in consumer electronics
 - Cellphone baseband processors
 - Wifi, GPS, etc....

Software Radio Vision



[Schreier, "ADCs and DACs: Marching Towards the Antenna," GIRAFE workshop, ISSCC 2003]

Software Radio Reality



[Schreier, "ADCs and DACs: Marching Towards the Antenna," GIRAFE workshop, ISSCC 2003]



Shameless Plug

□ If you are interested in how Analog to digital converters work and how to make them

□ Take ESE 568!

Good to know both sides of the system



Future of ADC design

- Today's ADCs are extremely well optimized
- □ For non-incremental improvements, we must explore new ideas in signal processing that tackle ADC inefficiency at the system level
 - Compressed sensing
 - Finite innovation rate sampling
 - Other ideas?

Filter Design Example



Optimal Filter Design

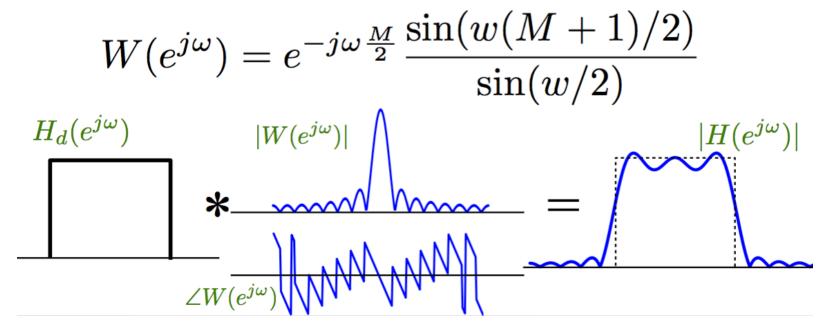
- Window method
 - Design Filters heuristically using windowed sinc functions
- Optimal design
 - Design a filter h[n] with $H(e^{j\omega})$
 - Approximate $H_d(e^{j\omega})$ with some optimality criteria or satisfies specs.

FIR Design by Windowing

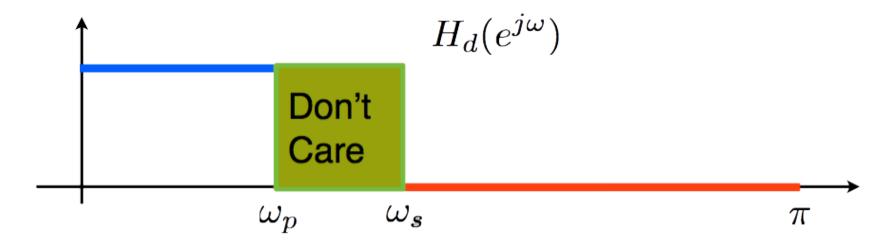
Desired filter,

$$H(e^{j\omega}) = H_d(e^{j\omega}) * W(e^{j\omega})$$

□ For Boxcar (rectangular) window



FIR Design by Optimality



□ Least Squares:

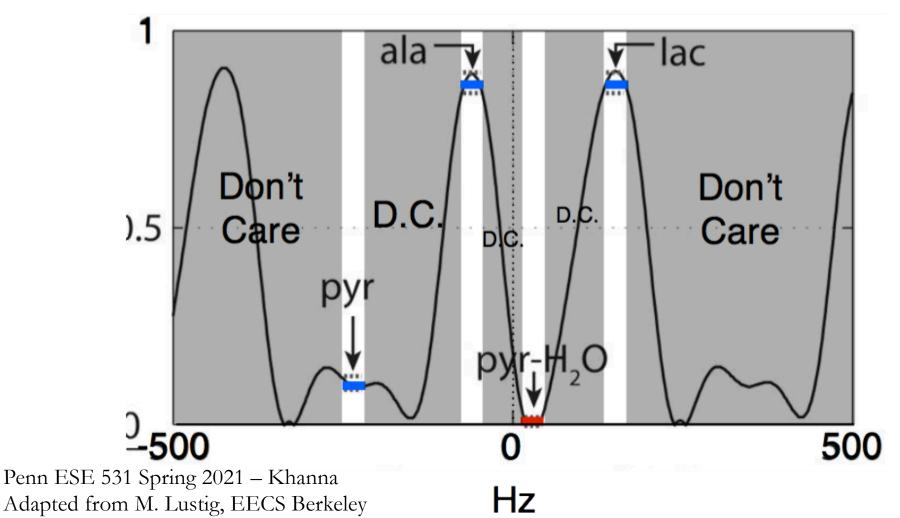
minimize
$$\int_{\omega \in \text{care}} |H(e^{j\omega}) - H_d(e^{j\omega})|^2 d\omega$$

Variation: Weighted Least Squares:

minimize
$$\int_{-\pi}^{\pi} W(\omega) |H(e^{j\omega}) - H_d(e^{j\omega})|^2 d\omega$$

Example of Complex Filter

- Larson et. al, "Multiband Excitation Pulses for Hyperpolarized 13C Dynamic Chemical Shift Imaging" JMR 2008;194(1):121-127
- Need to design 11 taps filter with following frequency response:





Admin

- □ Find web, get text, start HW 0 and assigned reading...
 - http://www.seas.upenn.edu/~ese531
 - https://piazza.com/upenn/spring2021/ese531/
 - https://canvas.upenn.edu/
- □ Accessibility Survey due 1/24
- □ Diagnostic quiz due 1/31
 - Use for review of material