ESE532: System-on-a-Chip Architecture

Day 2: September 8, 2021 Analysis, Metrics, and Bottlenecks

Work Preclass Lecture start 10:20am

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Today: Analysis

- How do we quickly estimate what's possible?
 - Before developing a complete solution
 - less effort than developing complete solution
- · How should we attack the problem?
 - Achieve the performance, energy goals?
- When we don't like the performance we're getting, how do we understand it?
- · Where should we spend our time?

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Today: Analysis

- Part 1: Key Terms and Concepts
 - Throughput
 - Latency
 - Bottleneck
- · Part 2: Broader view
 - Bottleneck
 - Computation as a Graph, Sequence
 - Critical Path
- Part 3: Time and Space
- Part 4: Limits
 - Resource Bound
 - And Critical Path Bound
 - 90/10 Rule (time permitting)

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Message for Day

- Identify the Bottleneck
 - May be in compute, I/O, memory, data movement
- · Focus and reduce/remove bottleneck
 - More resources
 - More efficient use of resources
- Repeat

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Latency vs. Throughput

- Latency: Delay from inputs to output(s)
- Throughput: Rate at which can produce new set of outputs
 - (alternately, can introduce new set of inputs)

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Preclass Washer/Dryer Example

• 10 shirt capacity



- 1 Washer Takes 30 minutes
- 1 Dryer Takes 60 minutes
- How long to do one load of wash?
 - → Wash latency
- · Cleaning Throughput?



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Pipeline Concurrency



- · Break up the computation graph into stages
 - Allowing us to
 - · reuse resources for new inputs (data),
 - while older data is still working its way through the graph
 - -Before it has exited graph
 - Throughput > (1/Latency)
- · Relate liquid in pipe
 - Doesn't wait for first drop of liquid to exit far end of pipe before accepting second drop

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Escalator Image Source: https://commons.wikimedia.org/wiki/File:Tanforan_Target_escalator_1.JPG 8

Escalator



- · Moves 2 ft/second
- Assume for simplicity one person can step on escalator each second
- Escalator travels 30 feet (vertical and horizontal)
- · Latency of escalator trip?
- Throughput of escalator: people/hour?

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Bottleneck

- What is the rate limiting item?
 Resource, computation,
 - ricocaros, comparadori, riii

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Preclass Washer/Dryer Example

- 1 Washer Takes 30 minutes
 - Isolated throughput 20 shirts/hour
- 1 Dryer Takes 60 minutes
 - Isolated throughput 10 shirts/hour
- Where is bottleneck in our cleaning system?



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Preclass Washer/Dryer Example

1 Washer \$500

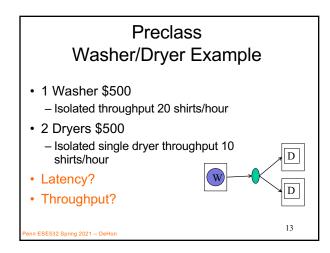


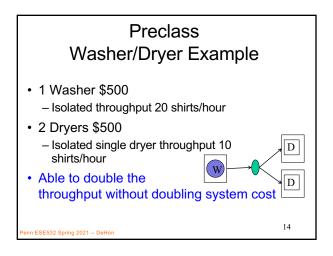
- Isolated throughput 20 shirts/hour
- 1 Dryer \$500
 - Isolated throughput 10 shirts/hour
- How do we increase throughput with \$500 investment

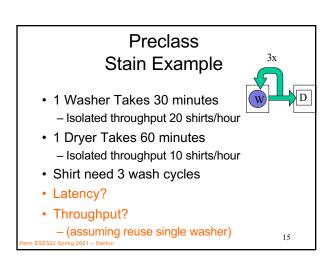


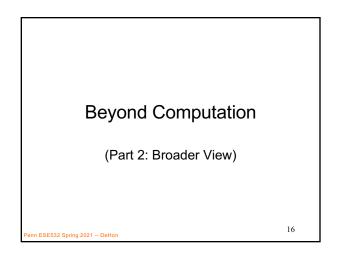
60m

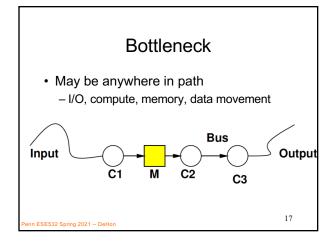
60m

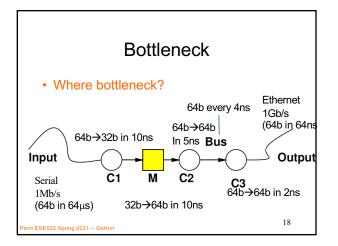


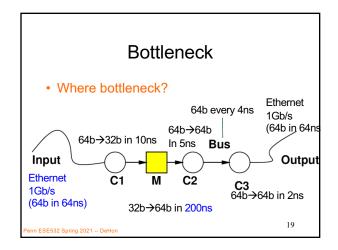


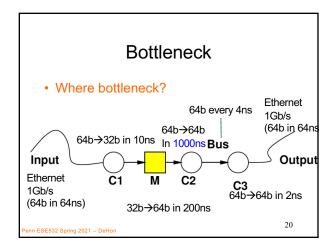












Feasibility / Limits

- First things to understand
 Obvious limits in system?
- · Impossible?
- Which aspects will demand efficient mapping?
- · Where might there be spare capacity

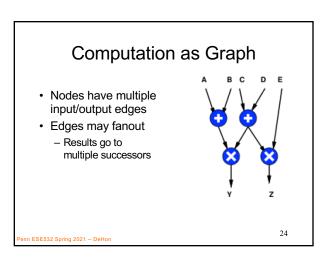
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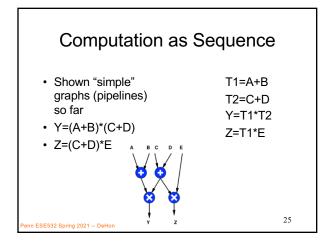
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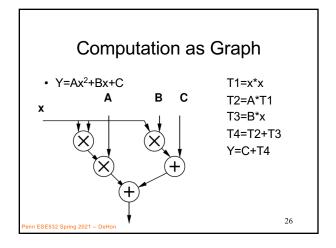
Generalizing (to more general task graphs)

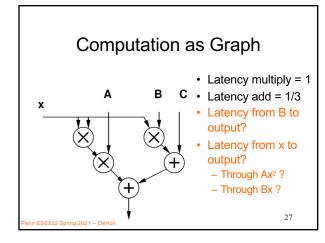
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Computation as Graph • Shown "simple" graphs (pipelines) so far • Y=(A+B)*(C+D) • Z=(C+D)*E Note: HW2 ask you to draw a dataflow graph. Here's an example...more to come. Penn ESE532 Spring 2021 - DeHon

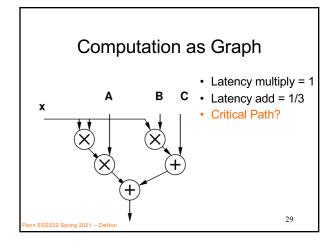


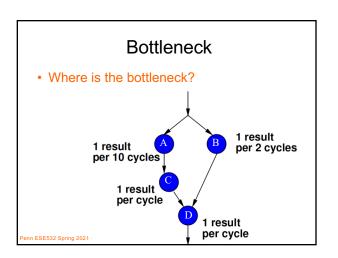






Delay in Graphs • Observe: There are multiple paths from inputs to outputs · Need to complete all of them to produce outputs · Limited by longest path · Critical path: longest path in the graph





Time and Space

(Part 3)

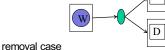
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Space

- "Space" is an abstract term for physical resources
 - On VLSI chip: Area mm² of silicon
 - On our FPGA: # of LUTs used
 - More abstractly: # of Adders, multipliers
 - Laundry example
 - \$\$ to spend on laundry equipment
 - Physical space (sq. ft) in laundry room

Space-Time

- In general, we can spend resources to reduce time
 - Increase throughput



Three wash stain removal case







Space Time

- Computation
 - -A=x0+x1
 - -B=x2+x3
 - C=A+B
- · Adder takes one cycle
- Latency on 3 adders?

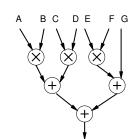
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Space Time

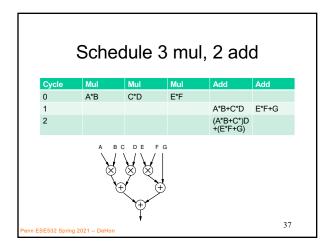
- Computation
 - -A=x0+x1
 - -B=x2+x3
 - C=A+B
- · Adder takes one cycle
- · Could perform on one adder
 - (like one washer)
 - Reuse adder in time
 - Let cycle time be one adder delay
- · Latency on one adder?

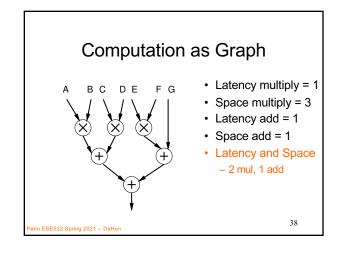
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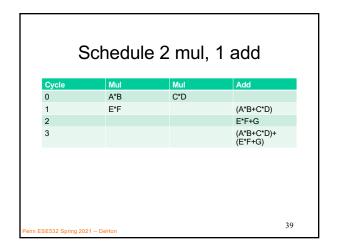
Computation as Graph

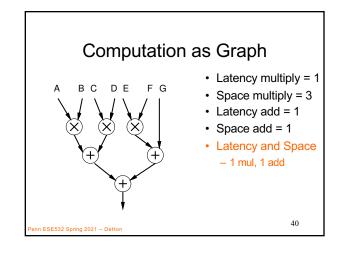


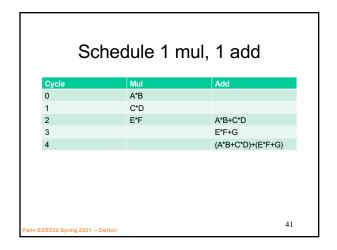
- Latency multiply = 1
- Space multiply = 3
- · Latency add = 1
- Space add = 1
- (can perform add or multiple in one cycle)
- Latency and Space - 3 mul, 2 add

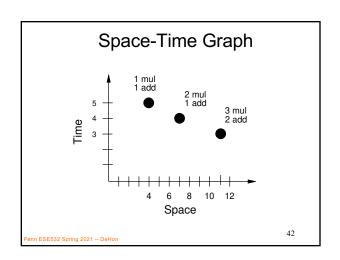


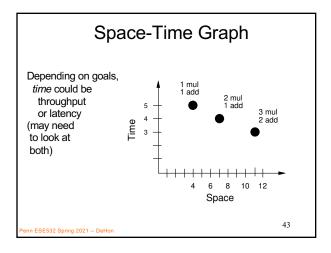












Two Bounds

Part 4: Limits (still in Time and Space)

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Problem

- Coming up with an exact time count can be hard (human/computer time consuming)
 - Technically a hard problem
 - NP-Complete: no known non-exponential solution
- Requires reasoning about structure of graph
- Would be nice to have a quick (easy) answer on what is feasible
 - ...and what is not feasible → impossible.

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Bounds

- · Establish the feasible range
 - Must be larger (or equal) than LB (lower bound)
 - Must be smaller (or equal) than UB (upper bound)
 - Solution will be between LB and UB
 - $-LB \le ActualTime \le UB$
- · Bounds in sports
 - Ball landing in-bounds or out-of bounds

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Bounds

- Quick lower bounds (LB) can estimate
 - -LB ≤ ActualTime
- Two:
 - CP: Critical Path
 - Sometimes call it "Latency Bound"
 - RB: Resource Capacity Bound
 - Sometimes call it "Throughput Bound" or "Compute Bound"

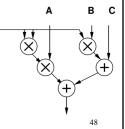
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Critical Path Lower Bound

- · Critical path assuming infinite resources
- Certainly cannot finish x any faster than that
- CP≤ *ActualTime*
- Ignores resource limits

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Resource Capacity Lower Bound

- Sum up all capacity required per resource: TotalOps = $\sum Ops$
 - E.g. number of multiplications, additions, memory lookups
- Divide by total resource (for type)
 - E.g., number of multipliers, adders, memory ports
 - $-RB = [TotalOps/Operators] \le ActualTime$
- Lower bound on compute
 - (best can do is pack all use densely)

- Ignores data dependency constraints

Multiple Resource Types

- $RB = Max([TotalOps_1/Operators_1],$ $[TotalOps_2/Operators_2],$)≤ ActualTime
- · Combine Critical Path Lower Bound $Max(CP, [TotalOps_1/Operators_1],$ $[TotalOps_2/Operators_2],$)≤ ActualTime

For Single Resource Type

- (and no communication time...)
- Can use to get upper bound:
- $ActualTime \leq CP + RB$
- · Together:
- $Max(CP, RB) \le ActualTime \le CP + RB$

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Washer-Dryer Bounds

· Task: wash & dry 30 shirts

• Washer: 10 shirts/30 min.

· Dryer: 10 shirts/60 min.

· 2 Washers, 2 Dryers

• W-->D CP=90 minutes

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Washer-Dryer Bounds

- Task: wash & dry 30 shirts CP=90 min
- · Washer: 10 shirts/30 min.
- Dryer: 10 shirts/60 min.
- · 2 Washers, 2 Dryers
- · Washer Bound:
 - WB= [30 shirts/(2 washers×10 shirts/washer)] \times 30 min = 60 min
- DB=[30 shirts/(2 dryers \times 10 $shirts/dryer) \times 60 min = 120 minutes$

Washer-Dryer Bounds

Task: wash & dry 30 shirts CP=90 min

• Washer: 10 shirts/30 min.

• Dryer: 10 shirts/60 min.

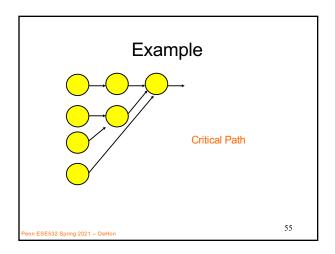
· 2 Washers, 2 Dryers RB=120 min

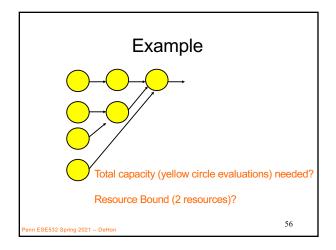
• Max(90,60,120)=120≤ TaskTime

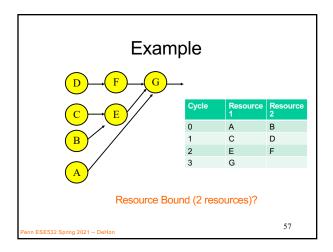
• TaskTime: 150

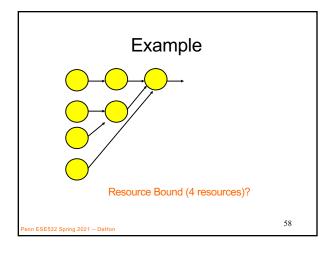
- 0 start 2 washes
- 30 start 2 dryers; start 1 wash
- 90 finish 2 dryers; start last dryer load

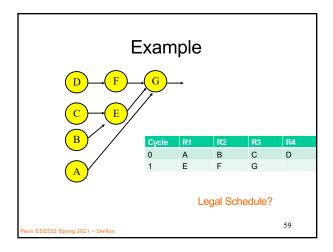
- 150 finish last dryer load





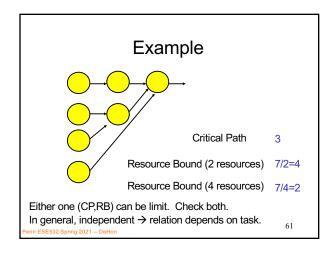






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- Lower bound on compute
 - (best can do is pack all use densely)
- Ignores data dependency constraints



What are the telling us

- If CP<RB
 - Adding resources (space) may be effective at reducing latency
- If RB<CP
 - Adding resources (space) will not reduce latency

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90/10 Rule (of Thumb)

- · Observation that code is not used uniformly
- 90% of the time is spent in 10% of the code
- Knuth: 50% of the time in 2% of the code
- · Implications
 - There will typically be a bottleneck
 - We don't need to optimize everything
 - We don't need to uniformly replicate space to achieve speedup
 - Not everything needs to be accelerated

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Big Ideas

- Identify the Bottleneck
 - May be in compute, I/O, memory ,data movement
- Focus and reduce/remove bottleneck
 - More resources
 - More efficient use of resources

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Admin

- · Reading for Day 3 on web
- HW1 due Friday
- HW2 out
 - Partner assignment (see canvas)
- · Wednesday office hours 7-8pm
 - (not 5-6pm as previously announced)
- Remember feedback
- Remaining Questions?

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