

ESE534: Computer Organization

Day 17: March 21, 2012
Interconnect 4: Switching

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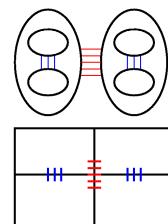
Previously

- Used Rent's Rule characterization to understand wire growth

$$IO = c N^p$$

- Top bisections will be $\Omega(N^p)$
- 2D wiring area

$$\Omega(N^p) \times \Omega(N^p) = \Omega(N^{2p})$$



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We Know

- How we avoid $O(N^2)$ wire growth for “typical” designs
- How to characterize locality
- How we might exploit that locality to reduce wire growth
- Wire growth implied by a characterized design

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Today

- Switching
 - Implications
 - Options
- Exploiting Multiple Metal Layer

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Switching:

How can we use the locality captured by Rent's Rule to reduce switching requirements? (How much?)

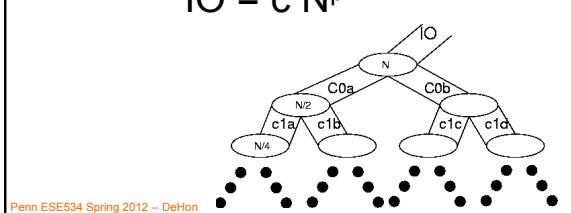
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Observation

- Locality that saved us wiring, also saves us switching

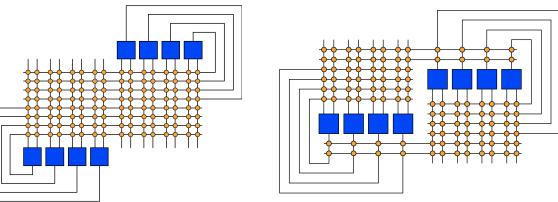
$$IO = c N^p$$



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Consider

- Crossbar case to exploit wiring:
 - split into two halves, connect with limited wires
 - $N/2 \times N/2$ crossbar each half
 - $N/2 \times c(N/2)^p$ connect to bisection wires

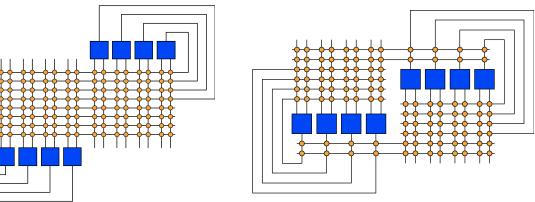


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Preclass 1

- $N=8, p=0$ crosspoints?

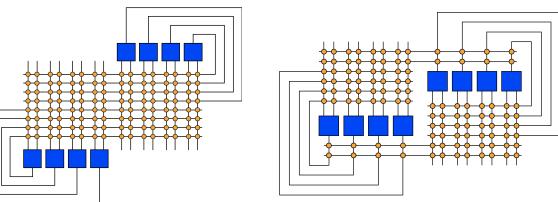


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Preclass 1

- $N=2^{15}, p=2/3$ crosspoints?

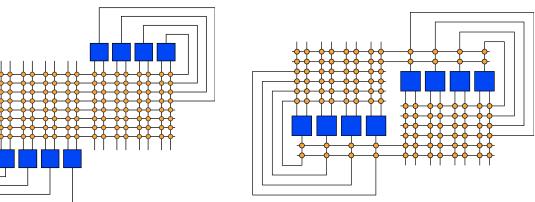


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Preclass 1

- Symbolic Ratio?
- Ratio $N=2^{15}, p=2/3$?



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What next

- What do we do when something works once?
- ...we try it again...

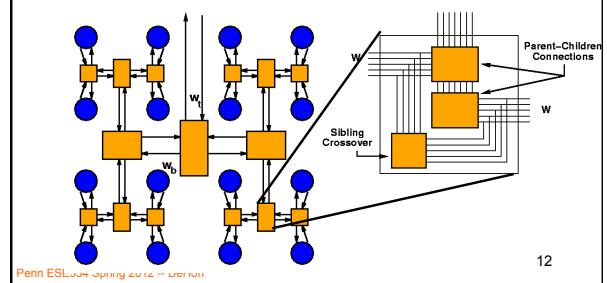


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Recurse

- Repeat at each level → form a tree



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Additional Structure

- This motivates us to look beyond crossbars
 - can we depopulate crossbars on up-down connection without loss of functionality?

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Can we do better?

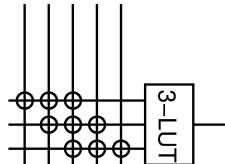
- Crossbar too powerful?
 - Does the specific down channel matter?
- What do we want to do?
 - Connect to *any* channel on lower level
 - Choose a subset of wires from upper level
 - order not important

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N choose K

- Exploit freedom to depopulate switchbox
- Can do with:
 - $K \times (N-K+1)$ switches
 - Vs. $K \times N$
 - Save $\sim K^2$



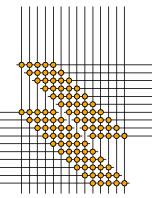
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N-choose-M

- Up-down connections
 - only require concentration
 - choose M things out of N
 - i.e. **order** of subset irrelevant
- Consequent:
 - can save a constant factor $\sim 2^p/(2^p-1)$
 - $(N/2)^p \times N^p$ vs $(N^p - (N/2)^p + 1)(N/2)^p$
 - $p=2/3 \rightarrow 2^p/(2^p-1) \approx 2.7$
- Similary, Left-Right
 - order not important \Rightarrow reduces switches

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Multistage Switching

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Multistage Switching

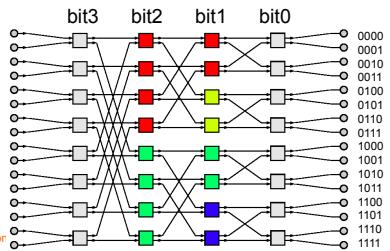
- We can route any **permutation** with fewer switches than a crossbar
- If we allow switching in stages
 - Trade increase in switches in path
 - For decrease in total switches

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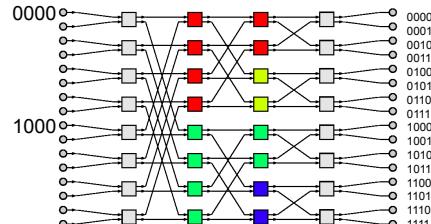
Butterfly

- Log stages
- Resolve one bit per stage



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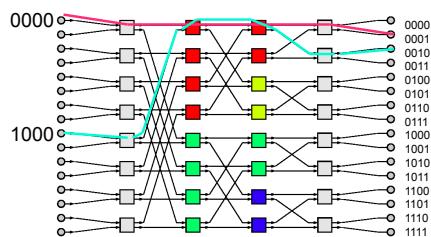
What can a Butterfly Route?



- $0000 \rightarrow 0001$
- $1000 \rightarrow 0010$

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What can a Butterfly Route?



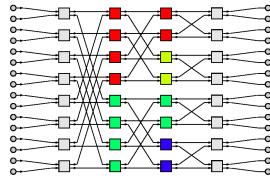
- $0000 \rightarrow 0001$
- $1000 \rightarrow 0010$

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Butterfly Routing

- **Cannot** route all permutations
– Get internal blocking

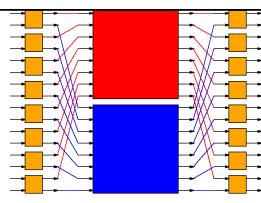


- What required for non-blocking network?

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Decomposed Routing

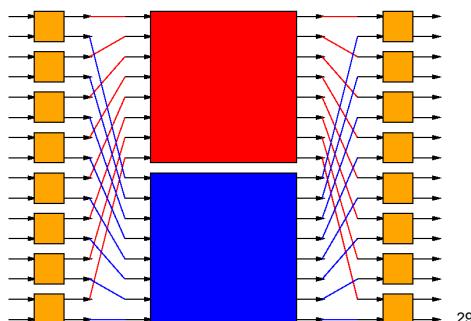
- Pick a link to route.
- Route to destination over red network
- At destination,
 - What can we say about the link which shares the final stage switch with this one?
 - What can we do with link?
- Route that link
 - What constraint does this impose?
 - So what do we do?



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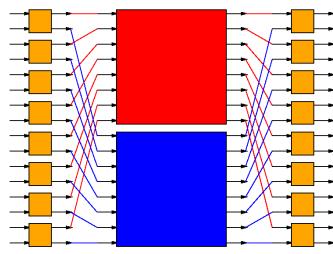
Decomposition



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Decomposition

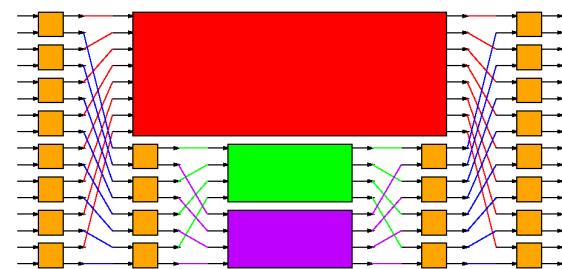
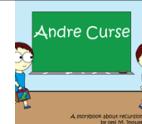
- Switches: $N/2 \times 2 \times 4 + (N/2)^2 < N^2$



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Recurse

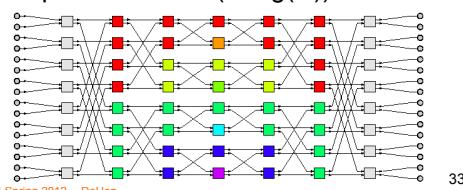
If it works once, try it again...



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Result: Beneš Network

- $2\log_2(N)-1$ stages (switches in path)
- Made of $N/2$ 2×2 switchpoints
– (4 switches)
- $4N \times \log_2(N)$ total switches
- Compute route in $O(N \log(N))$ time

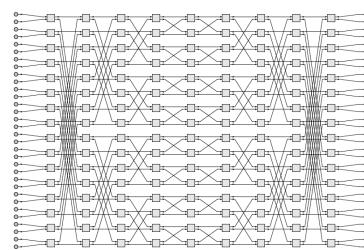


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Preclass 2

- Switches in Benes 32?
- Ratio to 32×32 Crossbar?

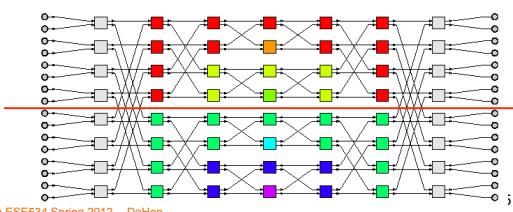


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Beneš Network Wiring

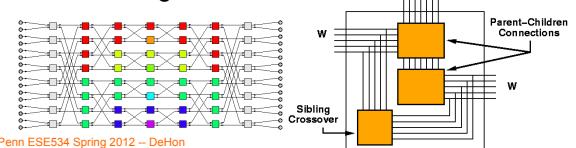
- Bisection: N
- Wiring $\rightarrow O(N^2)$ area (fixed wire layers)



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Beneš Switching

- Beneš reduced switches
– N^2 to $N(\log(N))$
– using multistage network
- Replace crossbars in tree with Beneš switching networks



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Beneš Switching

- Implication of Beneš Switching
 - still require $O(W^2)$ wiring per tree node
 - or a total of $O(N^{2p})$ wiring
 - now $O(W \log(W))$ switches per tree node
 - converges to $O(N)$ total switches!
 - $O(\log^2(N))$ switches in path across network**
 - strictly speaking, dominated by wire delay $\sim O(N^p)$
 - but constants make of little practical interest except for very large networks ☺

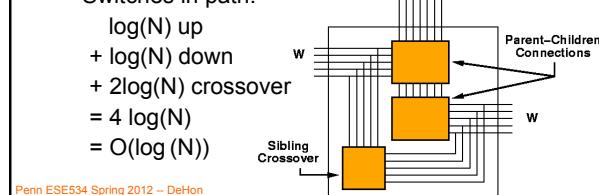
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Better yet...

- Believe do not need Beneš on the up paths
- Single switch on up path
- Beneš for crossover
- Switches in path:

$$\begin{aligned} & \log(N) \text{ up} \\ & + \log(N) \text{ down} \\ & + 2\log(N) \text{ crossover} \\ & = 4 \log(N) \\ & = O(\log(N)) \end{aligned}$$



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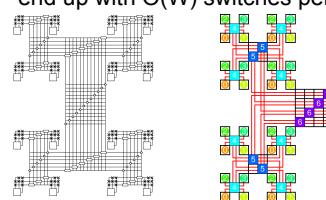
Linear Switch Population

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Linear Switch Population

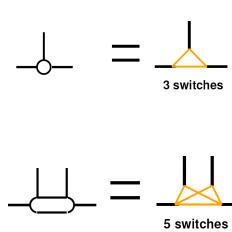
- Can further reduce switches
 - connect each lower channel to $O(1)$ channels in each tree node
 - end up with $O(W)$ switches per tree node



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Linear Switch ($p=0.5$)

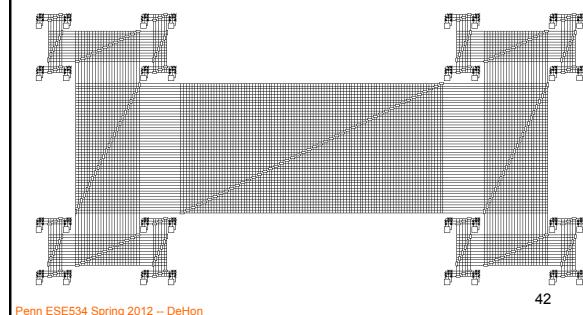
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Linear Population and Beneš

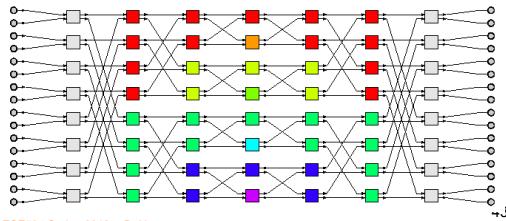
- Top-level crossover of $p=1$ is Beneš switching



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Beneš Compare

- Can permute stage switches so local shuffles on outside and big shuffle in middle



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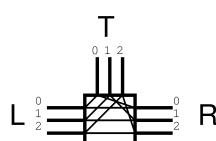
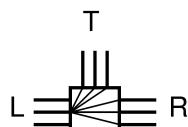
Linear Consequences: Good News

- Linear Switches
 - $O(\log(N))$ switches in path
 - $O(N^{2p})$ wire area
 - $O(N)$ switches
- More practical than Beneš crossover case

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Preclass 3



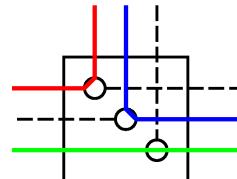
- Route Sets:
 - $T \rightarrow (L \& R)$, $L \rightarrow (T \& R)$, $R \rightarrow (T \& L)$
 - $T \rightarrow L$, $R \rightarrow T$, $L \rightarrow R$
 - $T \rightarrow L$, $L \rightarrow R$, $R \rightarrow L$

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Mapping Ratio

- Mapping ratio says
 - if I have W channels
 - may only be able to use W/MR wires
 - for a particular design's connection pattern
 - to accommodate any design
 - forall channels

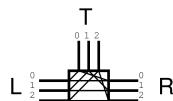
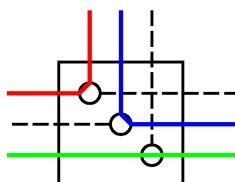


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physical wires $\geq MR \times \text{logical}$ 46

Mapping Ratio

- Example:
 - Shows $MR=3/2$
 - For Linear Population, 1:1 switchbox



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Linear Consequences: Bad News

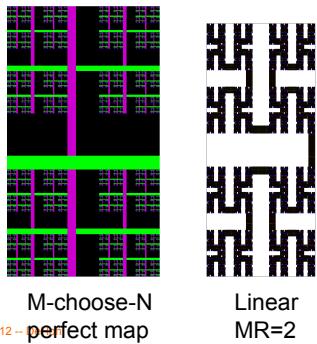
- Lacks guarantee can use all wires
 - as shown, at least mapping ratio > 1
 - likely cases where even **constant** not suffice
 - expect no worse than logarithmic
- Finding Routes is harder
 - no longer linear time, deterministic
 - **open as to exactly how hard**

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Area Comparison

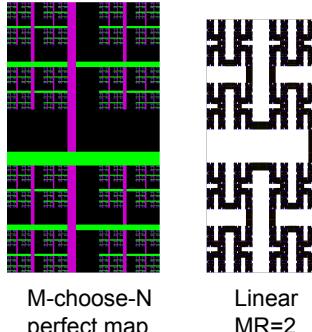
Both:
 $p=0.67$
 $N=1024$



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Area Comparison



M-choose-N
perfect map

Linear
MR=2

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Multilayer Metal

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Day 7

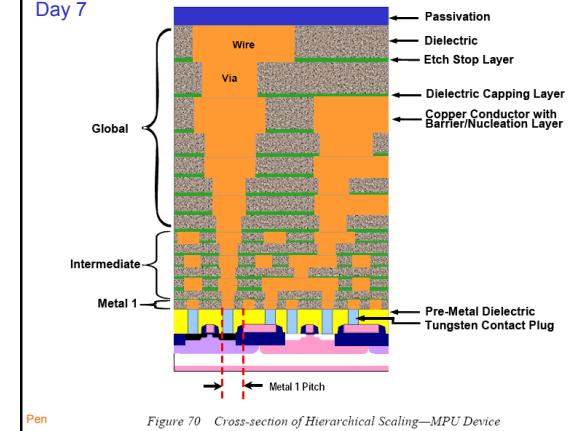
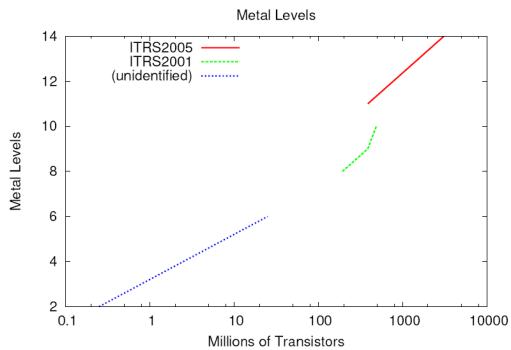


Figure 70 Cross-section of Hierarchical Scaling—MPU Device

Wire Layers = More Wiring



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Opportunity

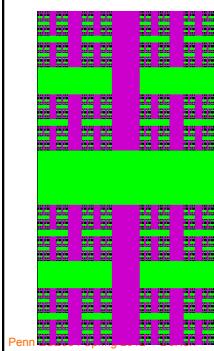
- Multiple Layers of metal allow us to
 - Increase effective pitch
 - Potentially route in 3D volume

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Day 18

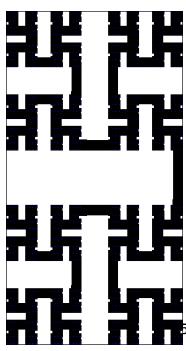
Larger “Cartoon”



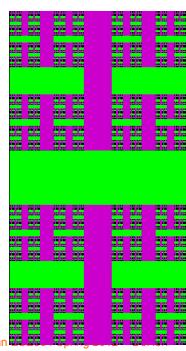
1024 LUT
Network

P=0.67

LUT
Area 3%



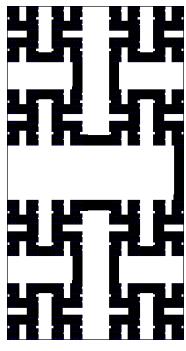
Challenge



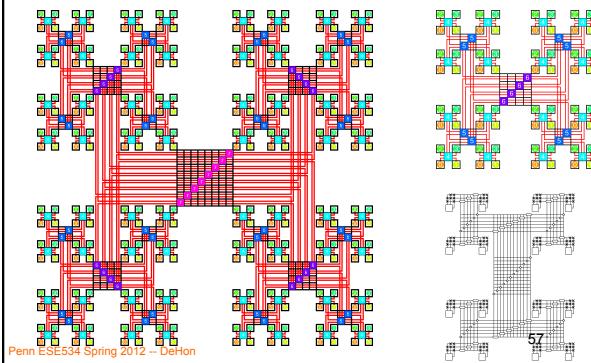
Spread or
balance
wires

Homogenize
switches

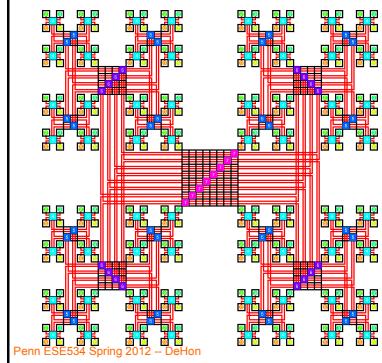
Avoid via
saturation



Linear Population Tree (P=0.5)

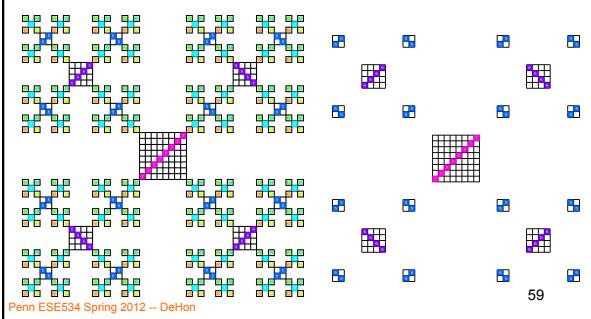


Linear Population Tree (P=0.5)



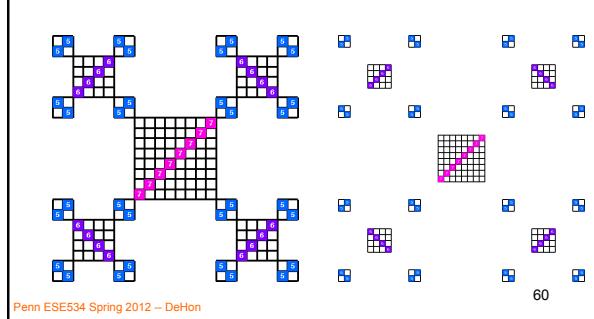
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BFT Folding



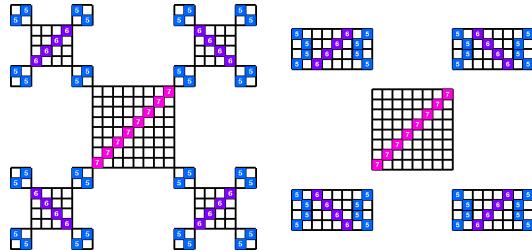
59

BFT Folding



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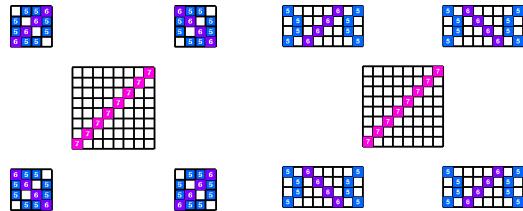
BFT Folding



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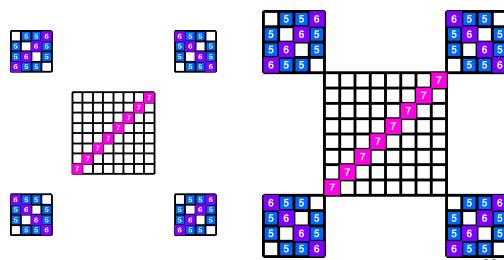
BFT Folding



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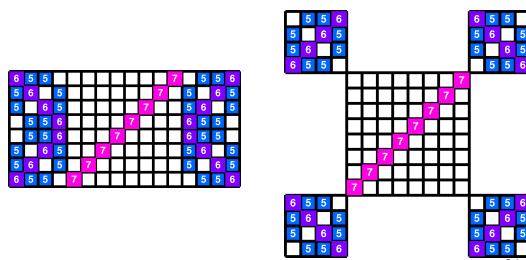
BFT Folding



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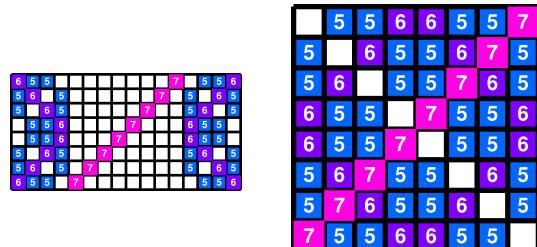
BFT Folding



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BFT Folding

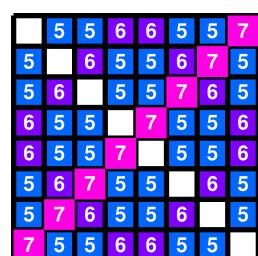


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Invariants

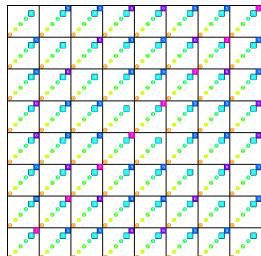
- Lower folds leave both diagonals free
- Current level consumes one, leaving other free



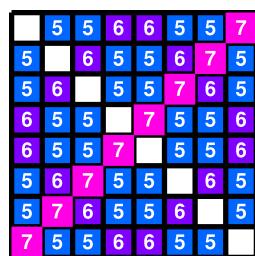
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BFT Folding

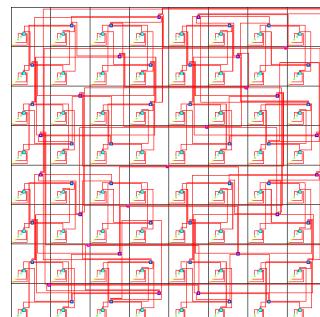


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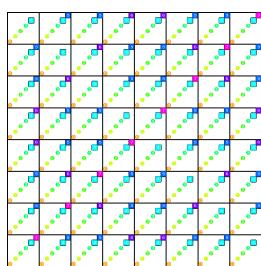
Wiring



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Avoid via Saturation

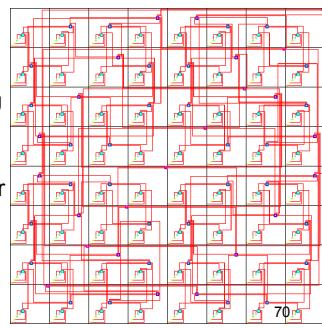


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Compact, Multilayer Linear Population Tree Layout

- Can layout BFT
 - in $O(N)$ 2D area
 - with $O(\log(N))$ wiring layers
- Can be extended for $p > 0.5$ as well
 - Wire layers grow as $O(N^{(p-0.5)})$



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Admin

- HW8 due Monday
- Reading for Monday on Web

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Big Ideas [MSB Ideas]

- In addition to wires, must have switches
 - Switches have significant area and delay
- Rent's Rule locality reduces
 - both wiring and switching requirements
- Naïve switches match wires at $O(N^{2p})$
 - switch area \gg wire area
 - prevent benefit from multiple layers of metal

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Big Ideas [MSB Ideas]

- Can achieve $O(N)$ switches
 - plausibly $O(N)$ area with sufficient metal layers
- Switchbox depopulation
 - save considerably on area (delay)
 - will waste wires
 - May still come out ahead ([evidence to date](#))