Problem: Place the following in 1D.

$$\begin{array}{l} A{=}i0{+}i1 \\ B{=}i1{+}i2 \\ C{=}i2{+}i3 \\ G{=}A{*}B \\ H{=}B{*}C \\ O{=}G{+}H \end{array}$$

Assume i0, i1 enter from left side, i2, i3 enters from right, and O exits to the right.

What is the:

- Channel Width
- Wire Length
- Squared Wire Length
- Critical Path Length (assuming gates cost unit delay and its costs unit delay to cross each position)