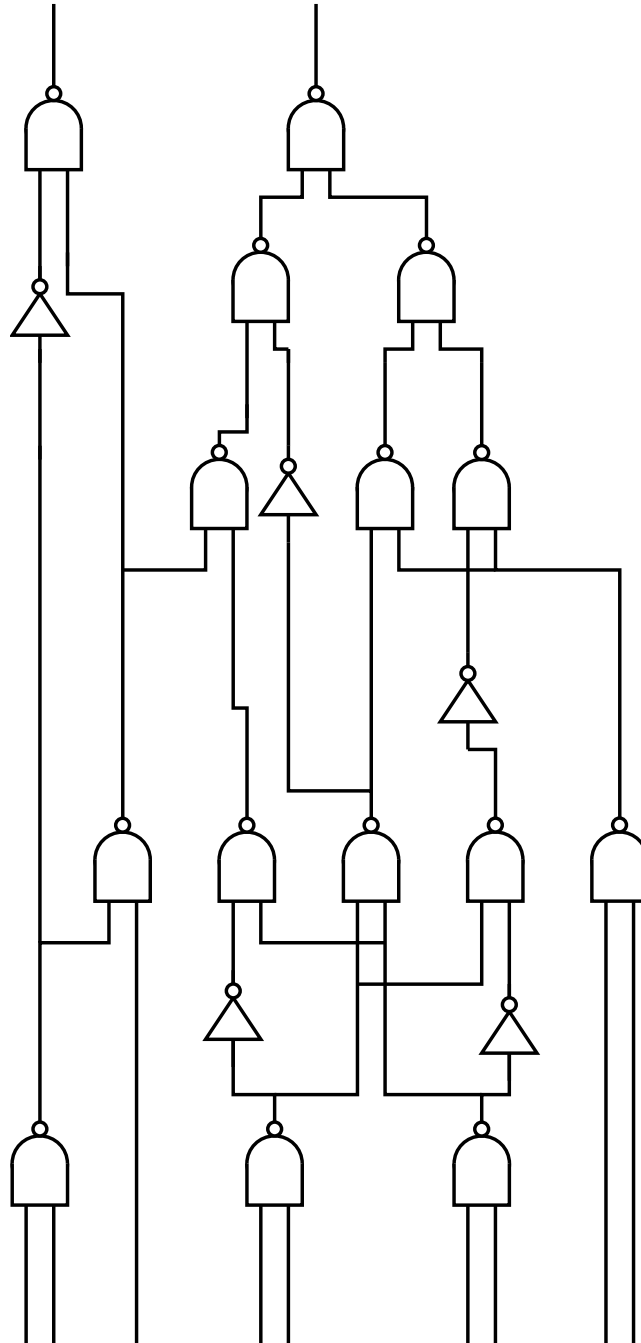


Problem: Cover the following circuit to minimize delay.

Library: All gates with 4 or fewer inputs.

- Delay of each gate in the library is 1.
- Assume delay independent of fanout.
- Area of each gate in the library is 1.



If have time: Cover the circuit to minimize area.