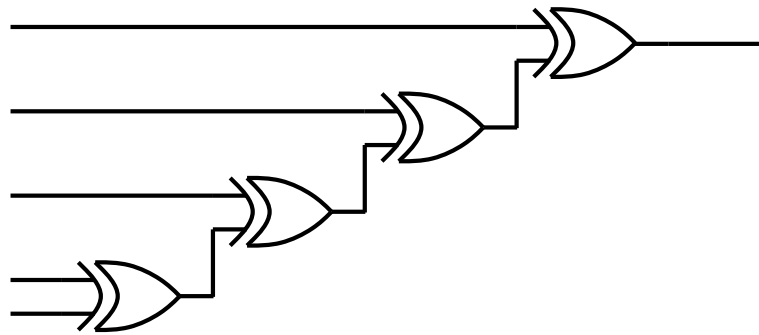


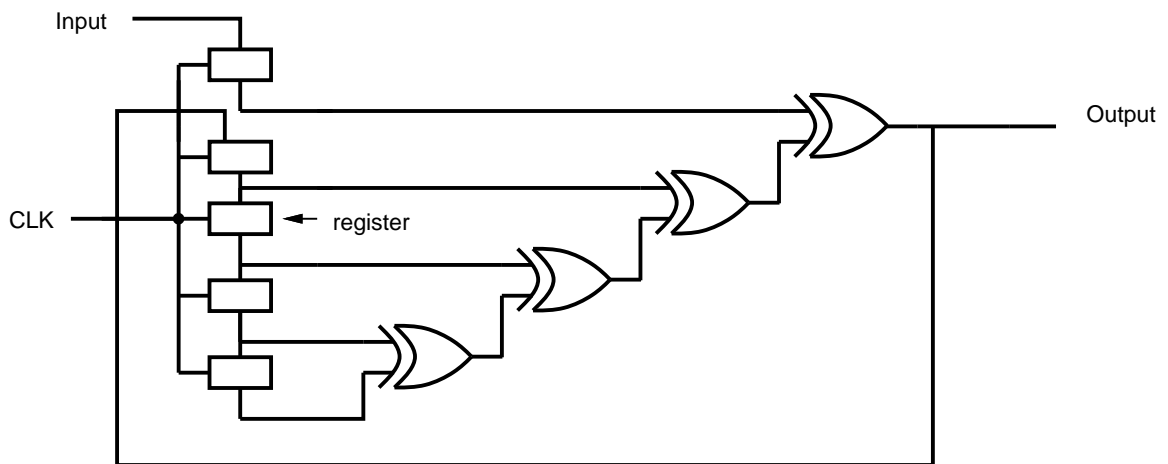
1. Consider the following circuit:



(a) What is the delay (unit delay XOR)?

(b) Add registers (pipeline) to run at a cycle time of 2 unit delays.

2. Consider the following circuit:



(a) What is the delay (unit delay XOR)?

(b) Can you reduce the delay of this circuit?

i. Show how.

ii. What is new delay?