Problem: Using the library of gates shown below, implement this netlist shown.


- What is the cost if you cover each drawn gate directly with its associated gate in the
library (i.e. use only inverter and nand2 gates)?
$\square$
- What portions of the graph can the nand3 cover?

- What assembly of gates has the least area?

- What area does this achieve? $\square$

Library (numbers on right are area cost of each gate)


2



