University of Pennsylvania

Department of Electrical and Systems Engineering ESE 216 LTSpice Transient Simulation and DC Sweep

This tutorial demonstrates the Transient simulation and DC sweep using LT Spice through an example of a half-wave rectifier.

Example



Fig. 1 A half-wave rectifier circuit

1: Design the schematic

To pick the diode from the design library, just click the "component" icon and choose the diode (Fig. 2). After placing the diode on the schematic, right click on the diode symbol and choose "Pick New Diode" to select the correct model for the diode (Fig. 3). For this example, we choose the 1N4148 diode model (Fig. 4), and click "OK" to confirm.

After inserting the diode, insert the load resistor $(1k\Omega)$, input voltage source, and make connections. The simulation schematic should look like Fig. 5 and the setup for the voltage source can be found in Fig. 6.

	17 Select Component Symbol				
Γ	Top Directory:	C:\Program Files (x86	i)\LTC\LTspiceIV\lib\sym 🔹		
			Diode Open this macromodel's test fixture diode		
	C. (Flogram File) [Comparators] [Digital] [FilterProducts] [Misc] [Opamps] [Optos] [PowerProducts] [References] [SpecialFunctions] bi bi2	bv cap csw current diode e e2 f FerriteBead FerriteBead2 g III	g2 h ind ind2 LED load load2 lpnp Itline mesfet njf	nmos nmos4 npn npn2 npn3 npn4 pjf pmos pmos4 pnp pnp2	

Fig. 2 Selecting the diode component symbol

Diode - D1	
ОК	
Cancel	□ D1
Pick New Diode	
Diode Properties	
Diode: D	\sim
Manufacturer:	
Туре:	
Average Forward Current[A]:	
Breakdown Voltage[V]: 0	

Fig. 3 Picking model for the diode

	-				×
					ОК
					Cancel
Part No.	Mfg.	type	Vbrkdn[V]	lave[A]	SPICE Model
1N914	OnSemi	silicon	75.0	0.20	.model 1N914 D(ls=
1N4148	OnSemi	silicon	75.0	0.20	.model 1N4148 D(l
MMSD4148	Onsemi	silicon	100.0	0.20	.model MMSD4148
1N5817	OnSemi	Schottky	20.0	1.00	.model 1N5817 D(l:
1N5818	OnSemi	Schottky	30.0	1.00	.model 1N5818 D(l:
1N5819	OnSemi	Schottky	40.0	1.00	.model 1N5819 D(l:
BAT54	Vishay	Schottky	30.0	0.30	.model BAT54 D(ls: 👻
•					•

Fig. 4 Choosing 1N4148 diode model



Fig. 5 Simulation schematic

Independent Voltage Source - V1	
Functions (none) PULSE(V1 V2 Tdelay Trise Tfall Ton Period Ncycles) SINE(Voffset Vamp Freq Td Theta Phi Ncycles) EXP(V1 V2 Td1 Tau1 Td2 Tau2) SEEM(Voff Vamp Ecar MDI Esig)	DC Value DC value: Make this information visible on schematic: Small signal AC analysis(.AC) AC Amplitude:
PWL(t1 v1 t2 v2) PWL FILE: Browse	AC Phase: Make this information visible on schematic: 📝
DC offset[V]: 0 Amplitude[V]: 5 Freq[Hz]: 1k Tdelay[s]: 0 Theta[1/s]: 0 Phi[deg]: 0 Ncycles: 0	Series Resistance[Ω]: Parallel Capacitance[F]: Make this information visible on schematic: I
Additional PWL Points Make this information visible on schematic: 📝	Cancel OK

Fig. 6 Setup the voltage source

2. Simulate and Plot the Schematic

After drawing the schematic and setting the component values, we can proceed to simulations. We can setup the simulation by choosing "Simulation" on the menu bar and select "Edit Simulation Command". This time we need to choose Transient simulation. The only thing we need to setup for the transient simulation is the stop time. Since we set the frequency of the voltage source to 1kHz, which corresponds to a period of 1ms, in order to see the full period signal in our simulation results, we should set the simulation "Stop Time" to at least two periods' time. In this example, I set the stop time to 3ms, which means we can see the signals for 3 periods.

ſ	Edit Simu	ulation Comn	nand				×
Γ	Transient	AC Analysis	DC sweep	Noise	DC Transfer	DC op pnt	
		Perf	om a non-lin	ear, time-	domain simulat	ion.	
	Stop Time: 3m						
		Т	ime to Start S	Saving D	ata:		
			Maximu	ım Times	tep:		
		Start external E	C supply vol	tages at	DV:		
	Stop simulating if steady state is detected: 🥅						
	Don't reset T=0 when steady state is detected:						
	Step the load current source:						
	Skip Initial operating point solution:						
	Syntax: .tran <tstop> [<option> []</option></tstop>						
	.tran 3m						
		Cancel	(OK			

Fig. 7 Transient simulation setup

After setting the simulation, we can just click on the "run" button and plot the output signal. The output signal should be like Fig. 8. Note that the transient simulation result has time as the horizontal axis and voltage as the vertical axis.



Fig. 8 Output signal from transient simulation

After we have done the transient simulation, we can move on to DC sweep simulations. The DC sweep is a function that sweep input voltage from a start value to a stop value for given steps. And it stores the DC voltage of every node in the circuit for each sweeping step. We can setup the DC sweep in the "Edit Simulation Command" window, as shown in Fig. 9. The "Name of Source to Sweep" is the name of the input source, V1, in our example. And the start value and stop value in the sweep is the voltage range of the sweep of the input voltage source. "Increment" is the step of the sweep. So the setting in Fig. 9 means we will have the input voltage source V1 to sweep from 0V to 5V at steps of 0.1V.

Keep in mind that the DC sweep disables the signal settings in the voltage source. This means even the signal source is set to output SINE wave at 1k Hz, the DC sweep simulation will automatically disable the SINE wave and make the signal source as a DC source sweeping from 0V to 5V at steps of 0.1V.

1	17 Edit Simulation Command					
ſ	Transient A	C Analysis DC sweep Noise DC Transfer DC op pnt				
	Compute the treating	DC operating point of a circuit while stepping independent sources and g capacitances as open circuits and inductances as short circuits.				
		1st Source 2nd Source 3rd Source				
	Name of 1st Source to Sweep: V1					
		Type of Sweep: Linear				
		Start Value: 0				
		Stop Value: 5				
L		Increment: 0.1				
	Syntax: .dc [<oct,dec,lin>] <source1> <start> <stop> [<incr>] [<source2>]</source2></incr></stop></start></source1></oct,dec,lin>					
	.dc V1 0 5 0.1					
	Cancel OK					

Fig. 9 DC sweep simulation setup

The result of the DC sweep simulation is shown in Fig. 10. Notice that the horizontal axis is the sweeping voltage from 0V to 5V, and the vertical axis is the output voltage. The plot is the output DC voltage corresponding to each step of the input sweeping DC voltage.



Fig. 10 DC sweep simulation plot